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(12) United States Patent

Mizuhara et al.

(54) COMPOSITE OF III-NITRIDE CRYSTAL ON LATERALLY STACKED SUBSTRATES

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patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

21) Appl. No.: **14/491,962**

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US 2015/0008563 A1 Jan. 8, 2015

Related U.S. Application Data

(63) Continuation-in-part of application No. 14/194,830, filed on Mar. 3, 2014, now Pat. No. 8,872,309, which is

(Continued)

(30) Foreign Application Priority Data

(51) Int. Cl. H01L 29/04 H01L 29/20

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(Continued)

(52) U.S. Cl.

CPC *H01L 29/045* (2013.01); *H01L 29/2003* (2013.01); *C30B 25/18* (2013.01);

(Continued)

(58) Field of Classification Search

(10) Patent No.: US 9,064,706 B2 (45) Date of Patent: Jun. 23, 2015

USPC 257/615, 627, 628, E29.003, E29.004, 257/E29.089, E33.003, E33.005; 117/58,

117/63, 94, 95, 952

See application file for complete search history.

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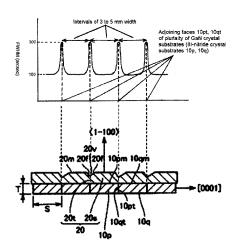
(Continued)

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(57) ABSTRACT

Group-III nitride crystal composites made up of especially processed crystal slices, cut from III-nitride bulk crystal, whose major surfaces are of $\{1\text{-}10\pm2\}$, $\{11\text{-}2\pm2\}$, $\{20\text{-}2\pm1\}$ or $\{22\text{-}4\pm1\}$ orientation, disposed adjoining each other sideways with the major-surface side of each slice facing up, and III-nitride crystal epitaxially present on the major surfaces of the adjoining slices, with the III-nitride crystal containing, as principal impurities, either silicon atoms or oxygen atoms. With x-ray diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto either of the major surfaces, FWHM peak regions are present at intervals of 3 to 5 mm width. Also, with threading dislocation density being measured along a <0001> direction of the III-nitride crystal substrate, threading-dislocation-density peak regions are present at the 3 to 5 mm intervals.

4 Claims, 26 Drawing Sheets



Related U.S. Application Data

a continuation of application No. 13/762,401, filed on Feb. 8, 2013, now Pat. No. 8,709,923, which is a continuation of application No. 13/564,731, filed on Aug. 2, 2012, now Pat. No. 8,404,042, which is a continuation-in-part of application No. 12/467,297, filed on May 17, 2009, now Pat. No. 8,258,051, which is a continuation-in-part of application No. PCT/JP2007/072096, filed on Nov. 14, 2007.

(51)	Int. Cl.	
	C30B 9/12	(2006.01)
	C30B 19/02	(2006.01)
	C30B 23/02	(2006.01)
	C30B 25/20	(2006.01)
	C30B 29/40	(2006.01)
	H01L 21/02	(2006.01)
	C30B 25/18	(2006.01)

(52) **U.S. Cl.**

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FIG. 1A

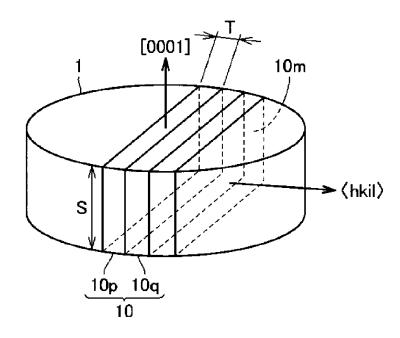


FIG. 1B

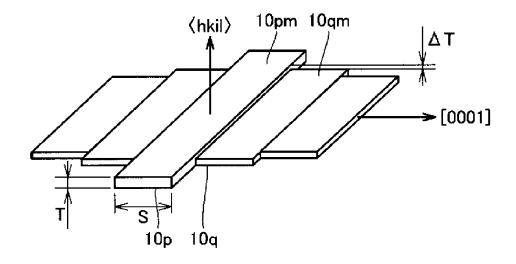


FIG. 1C

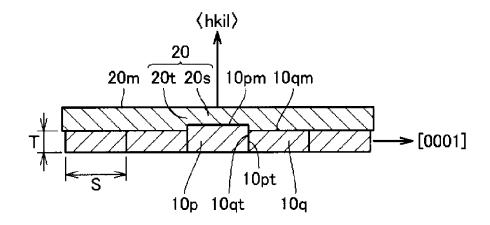


FIG. 2A

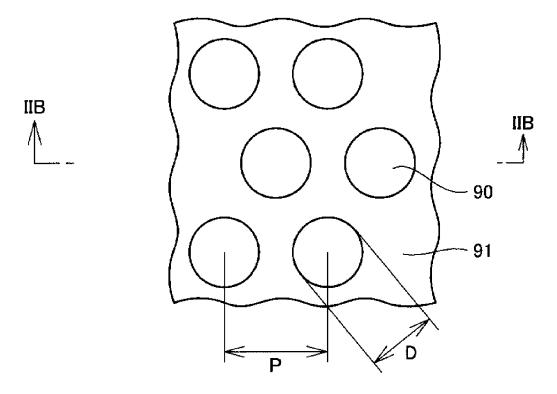


FIG. 2B

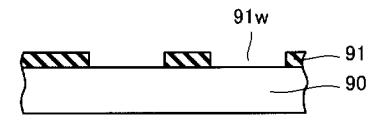


FIG. 3A

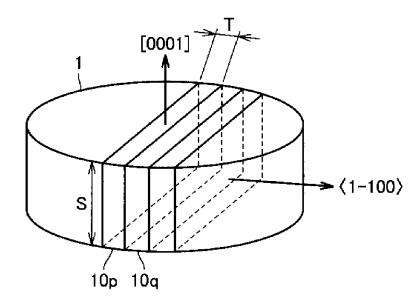


FIG. 3B

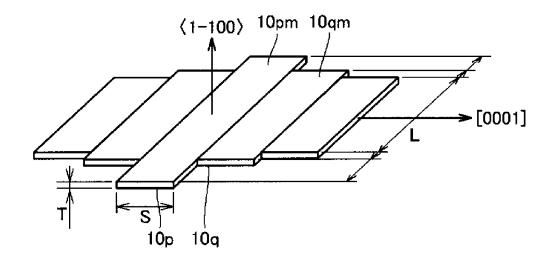


FIG. 3C

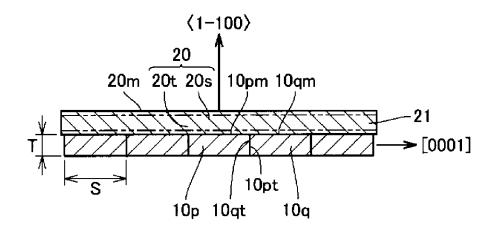


FIG. 4

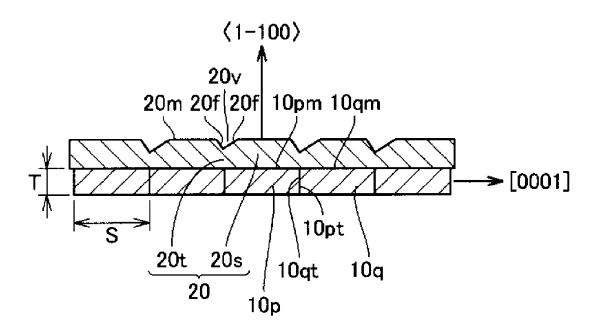


FIG. 5A

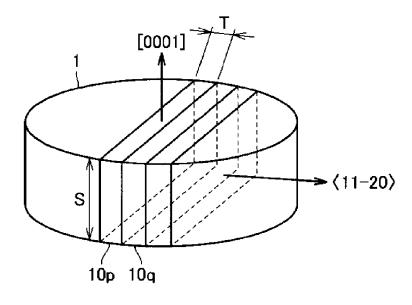


FIG. 5B

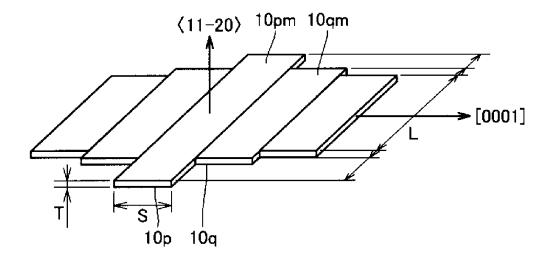


FIG. 5C

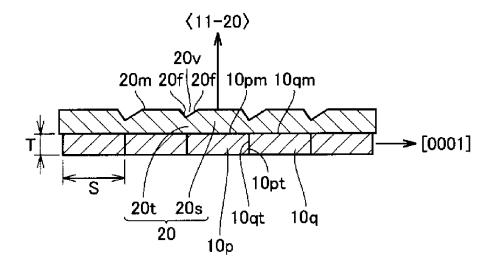


FIG. 6A

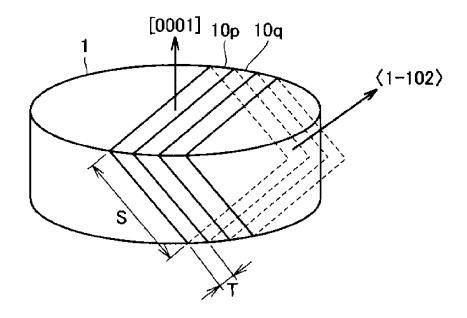


FIG. 6B

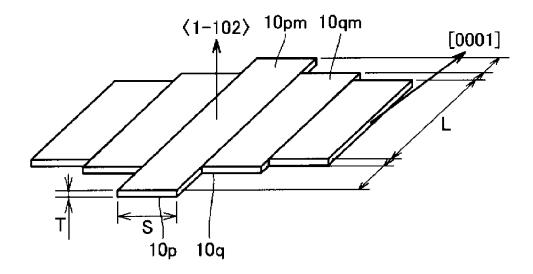


FIG. 6C

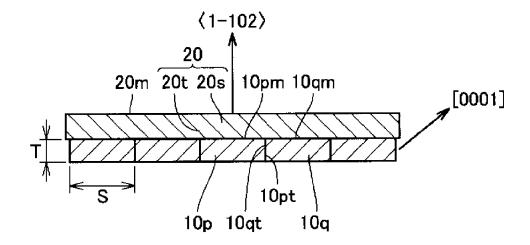


FIG. 6D

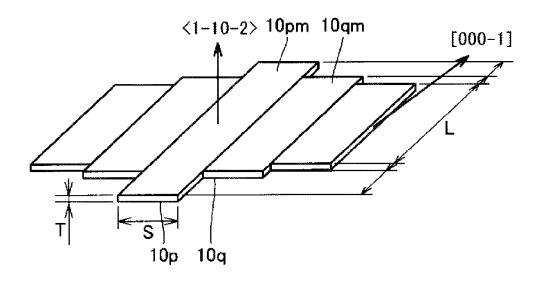


FIG. 6E

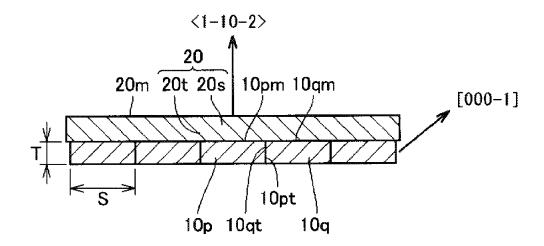


FIG. 7A

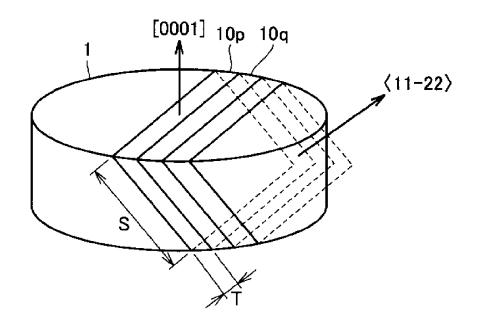


FIG. 7B

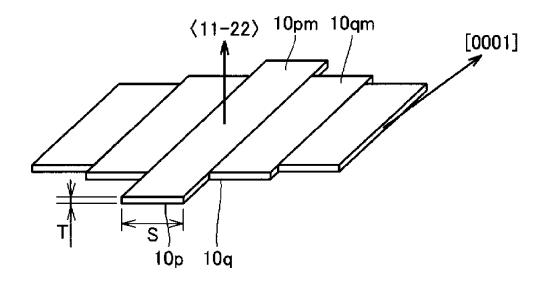


FIG. 7C

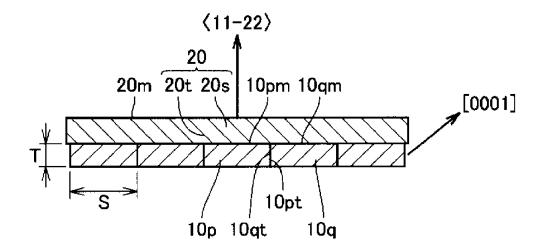


FIG. 7D

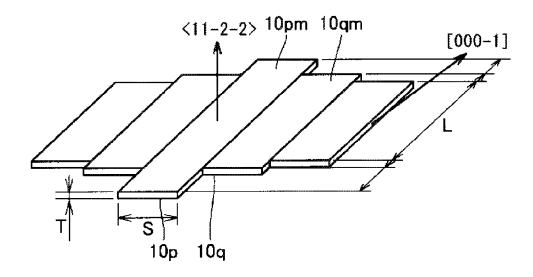


FIG. 7E

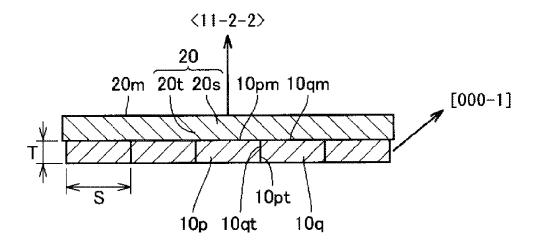


FIG. 8A

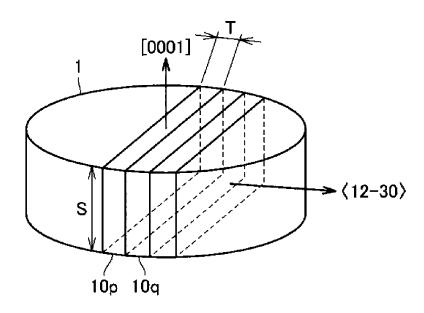


FIG. 8B

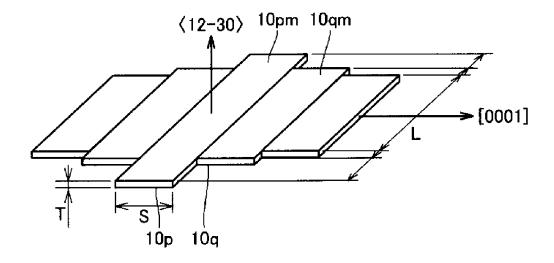


FIG. 8C

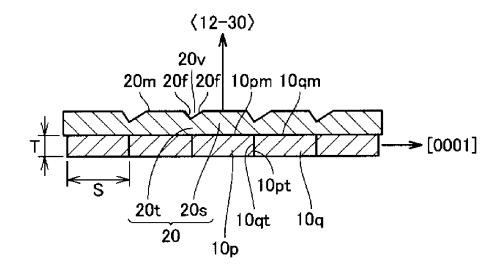


FIG. 9A

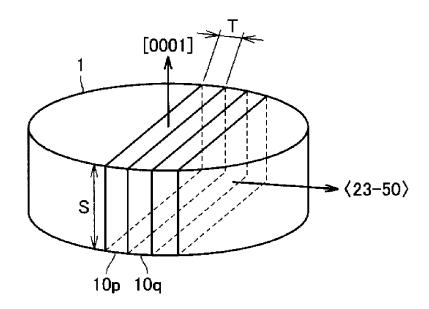


FIG. 9B

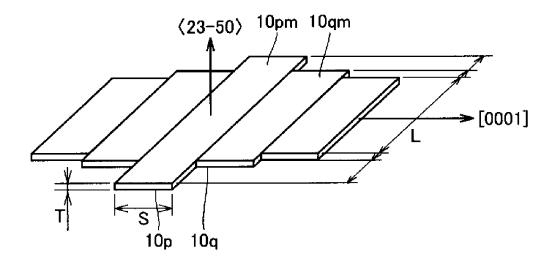


FIG. 9C

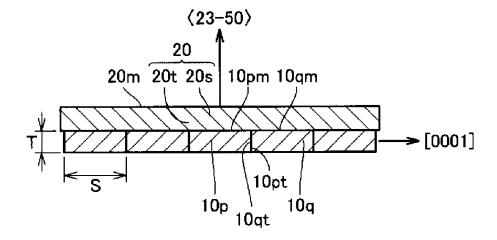


FIG. 10A

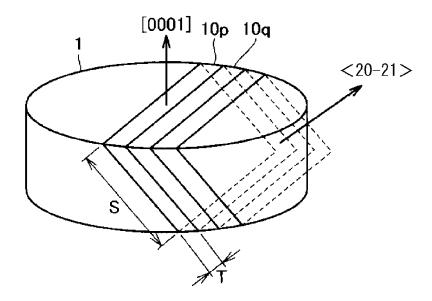


FIG. 10B

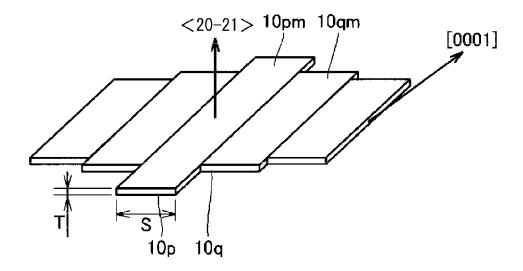


FIG. 10C

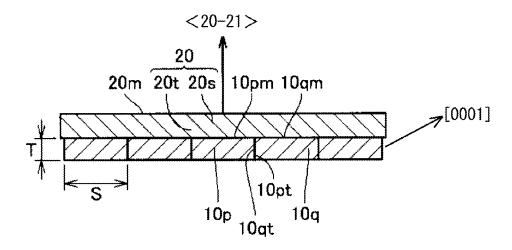


FIG. 10D

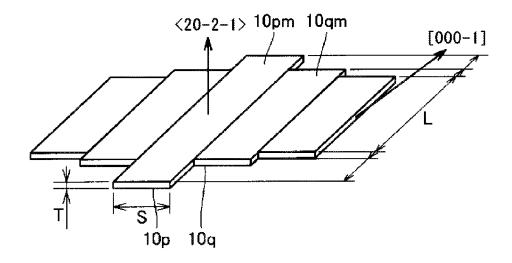


FIG. 10E

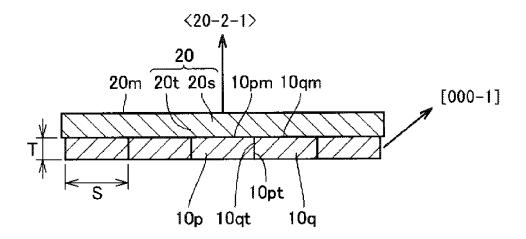


FIG. 11A

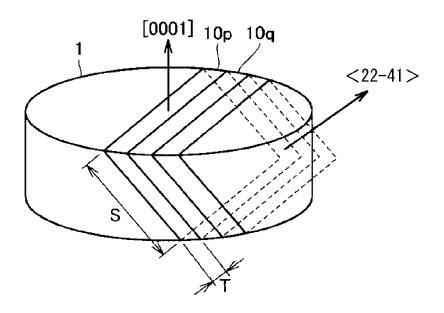


FIG. 11B

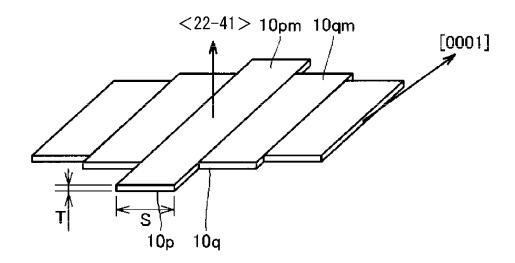


FIG. 11C

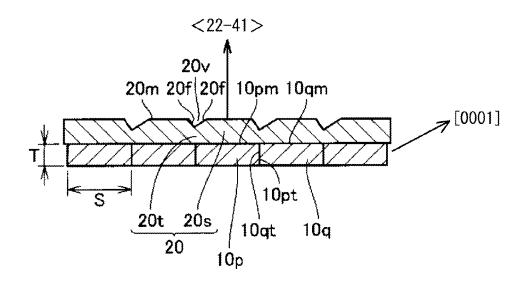


FIG. 11D

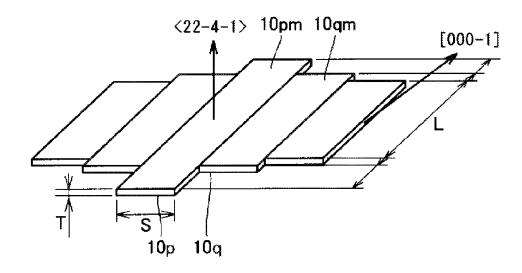


FIG. 11E

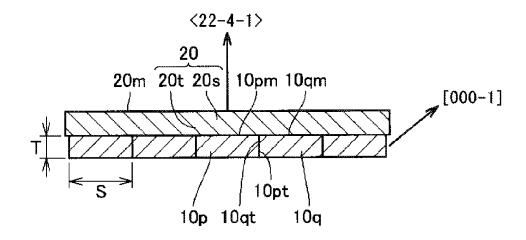


FIG. 12

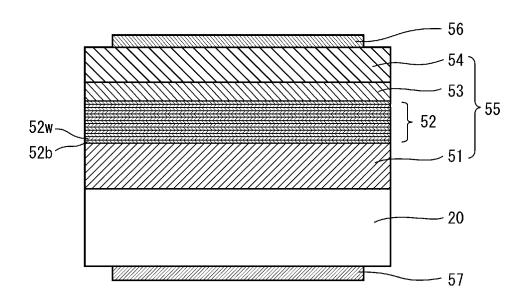


FIG. 13

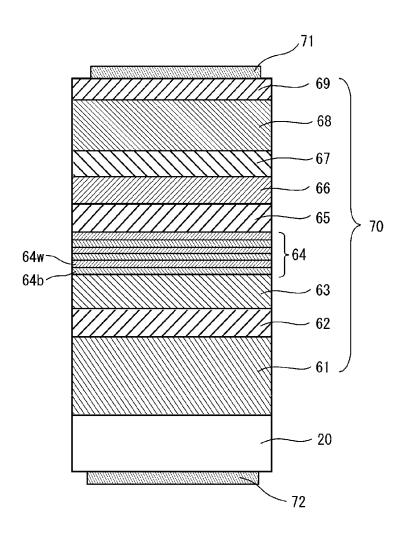


FIG. 14

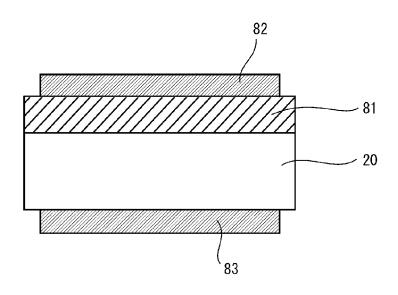


FIG. 15

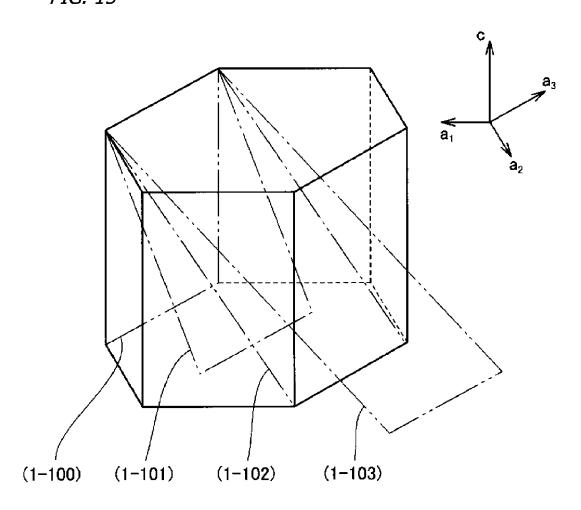


FIG. 16

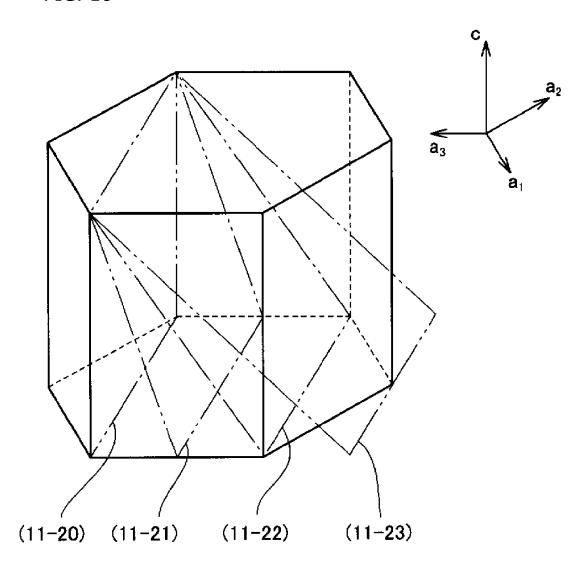


FIG. 17

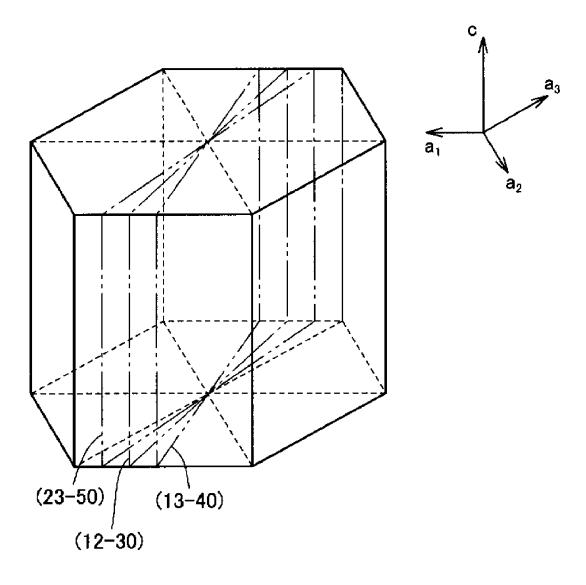
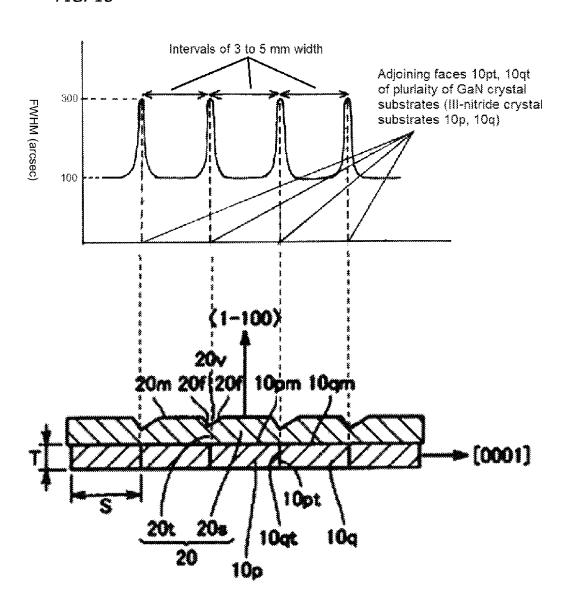


FIG. 18



COMPOSITE OF III-NITRIDE CRYSTAL ON LATERALLY STACKED SUBSTRATES

CROSS-REFERENCE TO RELATED APPLICATIONS

This is a continuation-in-part of application Ser. No. 14/194,830, filed on Mar. 3, 2014. Application Ser. No. 14/194,830 was a continuation of application Ser. No. 13/762,401, filed on Feb. 8, 2013, and now U.S. Pat. No. 10 8,709,923. Application Ser. No. 13/762,401 was a continuation of Ser. No. 13/564,731, filed on Aug. 2, 2012 and now U.S. Pat. No. 8,404,042. Application Ser. No. 13/564,731 was a continuation-in-part of application Ser. No. 12/467,297 filed on May 17, 2009 and now U.S. Pat. No. 8,258,051. 15 Application Ser. No. 12/467,297 was a continuation-in-part of International Application No. PCT/JP2007/072096, filed on Nov. 14, 2007.

BACKGROUND OF THE INVENTION

1. Technical Field

The present invention, involving Group-III nitride crystal manufacturing methods, relates to epitaxial III-nitride crystal in composites in which the epitaxial crystal has a major surface of plane orientation other than {0001}, designated by choice, and other special properties.

2. Description of the Related Art

Group-III nitride crystals, which are employed advantageously in semiconductor electronic devices including lightemitting devices and semiconductor sensors, are ordinarily manufactured by growing crystal onto the major surface of a sapphire substrate having a (0001) plane major surface, or onto a GaAs substrate having a (111) A-plane major surface, by means of a vapor-phase technique such as hydride vaporphase epitaxy (HVPE) or metalorganic chemical vapor deposition (MOCVD), or by flux growth or other liquid-phase technique. Consequently, ordinarily obtained III-nitride crystals have a major surface whose plane orientation is {0001}.

With light-emitting devices on substrates that are III-nitride crystal having a major surface whose plane orientation is {0001}, and in which a multiquantum-well (MQW) structure as a light-emitting layer has been deposited on the major surface, the light-emission efficiency is decreased by spontaneous polarization that occurs within the light-emitting layer 45 owing to the III-nitride crystal's <0001> oriented polarity. Consequently, the manufacture of III-nitride crystal having a major surface whose plane orientation is other than {0001} is being sought.

Reference is made, for example, to Japanese Unexamined 50 Pat. App. Pub. No. 2005-162526: The following method has been proposed as way of preparing gallium-nitride crystal having a surface plane orientation of choice, without influencing the plane orientation of the major surface of the substrate. Namely, according to the method disclosed in Pat. App. 55 Pub. No. 2005-162526, a number of rectangular crystal masses are diced from GaN crystal grown by vapor deposition. Meanwhile, a silicon oxide film is coated onto the surface of a separately readied sapphire substrate, and subsequently a number of recesses reaching to the substrate are 60 formed in the film. Next, the numerous crystal masses are embedded into the recesses in a manner such that their top surfaces will have the same plane orientation. Then, by vapor deposition with the crystal masses as seeds, gallium nitride crystal having a surface plane orientation of choice is grown. 65

With the method in Pat. App. Pub. No. 2005-162526, however, inasmuch as growth of the GaN crystal is carried out 2

with, as seeds, the masses of crystal GaN that have been embedded into the sapphire substrate, due to the disparity in thermal expansion coefficient between sapphire and GaN, fractures and strains appear in the GaN crystal when the crystal is cooled following the growth process, such that GaN crystal of superior crystallinity has not been obtainable.

Furthermore, if III-nitride crystal containing Al—for example, $Al_xGa_yIn_{1..x.y}N$ (x>0, $y\ge0$, $x+y\ge1$)—is grown by the method in Pat. App. Pub. No. 2005-162526, because the Al precursor is not selective with respect to the silicon oxide film, the $Al_xGa_yIn_{1..x.y}N$ grows onto the silicon oxide film as well, and consequently $Al_xGa_yIn_{1..x.y}N$ crystal of superior crystallinity has not been obtainable.

BRIEF SUMMARY OF THE INVENTION

An object of the present invention, to resolve the problems discussed above, is to make available epitaxial III-nitride crystal of superior crystallinity, in composites in which the epitaxial crystal has a major surface of plane orientation other than {0001}, designated by choice, and other special properties. The III-nitride crystal can be utilized to make available semiconductor devices that excel in light-emission efficiency.

In accordance with the present invention, contemplated is III-nitride crystal substrates having major surfaces of plane orientation other than {0001}, characterized by the property that with x-ray diffraction full-width-at-half-maximums (FWHMs) being measured along an axis defined by a <0001> direction of the substrate projected onto either of the major surfaces, FWHM peak regions are present at intervals of 3 to 5 mm width.

Further contemplated in accordance with the present invention is that in the foregoing III-nitride crystal substrates, with threading dislocation density being measured along a <0001> direction of the III-nitride crystal substrates, threading-dislocation-density peak regions are present at intervals of 3 to 5 mm width.

s have a major surface whose plane orientation is {0001}.

With light-emitting devices on substrates that are III-ni- 40 de crystal having a major surface whose plane orientation is x-ray diffraction peaks with divisions in the tip.

BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWINGS

FIG. 1A, a schematic diagram for illustrating one mode of embodying a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of slicing off substrates.

FIG. 1B, a schematic diagram for illustrating one mode of embodying a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of arranging substrates into a row.

FIG. 1C, a schematic diagram for illustrating one mode of embodying a III-nitride crystal manufacturing method involving the present invention, is a sectional view summarily representing a crystal-growth process step.

FIG. 2A, a diagram summarily illustrating an undersubstrate for growing bulk III-nitride crystal, represents a schematic plan view.

FIG. **2**B, a diagram summarily illustrating an undersubstrate for growing bulk III-nitride crystal, represents a schematic sectional view along IIB-IIB in FIG. **2**A.

FIG. 3A, a schematic diagram for illustrating one example of a III-nitride crystal manufacturing method involving the

present invention, is an oblique perspective view summarily representing a process step of slicing off substrates.

- FIG. 3B, a schematic diagram for illustrating one example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of arranging substrates into a row.
- FIG. 3C, a schematic diagram for illustrating one example of a III-nitride crystal manufacturing method involving the present invention, is a sectional view summarily representing a crystal-growth process step.
- FIG. 4 is a sectional view summarily representing a crystalgrowth process step in another example of a III-nitride crystal manufacturing method involving the present invention.
- FIG. **5**A, a schematic diagram for illustrating still another example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of slicing off substrates.
- FIG. 5B, a schematic diagram for illustrating the still other example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of arranging substrates into a row.
- FIG. 5C, a schematic diagram for illustrating the still other example of a III-nitride crystal manufacturing method involving the present invention, is a sectional view summarily representing a crystal-growth process step.
- FIG. 6A, a schematic diagram for illustrating yet another example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of slicing off substrates.
- FIG. 6B, a schematic diagram for illustrating the yet other example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of arranging substrates into 35 a row.
- FIG. 6C, a schematic diagram for illustrating the yet other example of a III-nitride crystal manufacturing method involving the present invention, is a sectional view summarily representing a crystal-growth process step.
- FIG. 6D, a schematic diagram for illustrating yet another example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of arranging substrates into a row.
- FIG. 6E, a schematic diagram for illustrating the yet other example of a III-nitride crystal manufacturing method involving the present invention, is a sectional view summarily representing a crystal-growth process step.
- FIG. 7A, a schematic diagram for illustrating a further 50 example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of slicing off substrates.
- FIG. 7B, a schematic diagram for illustrating the further example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of arranging substrates into a row.
- FIG. 7C, a schematic diagram for illustrating the further example of a III-nitride crystal manufacturing method involving the present invention, is a sectional view summarily representing a crystal-growth process step.
- FIG. 7D, a schematic diagram for illustrating the further example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of arranging substrates into a row.

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- FIG. 7E, a schematic diagram for illustrating the further example of a III-nitride crystal manufacturing method involving the present invention, is a sectional view summarily representing a crystal-growth process step.
- FIG. 8A, a schematic diagram for illustrating a still further example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of slicing off substrates.
- FIG. 8B, a schematic diagram for illustrating the still further example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of arranging substrates into a row.
- FIG. **8**C, a schematic diagram for illustrating the still further example of a III-nitride crystal manufacturing method involving the present invention, is a sectional view summarily representing a crystal-growth process step.
- FIG. 9A, a schematic diagram for illustrating a yet further example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of slicing off substrates.
- FIG. 9B, a schematic diagram for illustrating the yet further example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of arranging substrates into a row.
- FIG. 9C, a schematic diagram for illustrating the yet further example of a III-nitride crystal manufacturing method involving the present invention, is a sectional view summarily representing a crystal-growth process step.
- FIG. 10A, a schematic diagram for illustrating an even further example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of slicing off substrates.
- FIG. 10B, a schematic diagram for illustrating the even further example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of arranging substrates into a row.
- FIG. 10C, a schematic diagram for illustrating the even further example of a III-nitride crystal manufacturing method involving the present invention, is a sectional view summarily representing a crystal-growth process step.
- FIG. 10D, a schematic diagram for illustrating the even further example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of arranging substrates into a row.
- FIG. 10E, a schematic diagram for illustrating the even further example of a III-nitride crystal manufacturing method involving the present invention, is a sectional view summarily representing a crystal-growth process step.
- FIG. 11A, a schematic diagram for illustrating a yet even further example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of slicing off substrates.
- FIG. 11B, a schematic diagram for illustrating the yet even further example of a III-nitride crystal manufacturing method involving the present invention, is an oblique perspective view summarily representing a process step of arranging substrates into a row.
- FIG. 11C, a schematic diagram for illustrating the yet even further example of a III-nitride crystal manufacturing method involving the present invention, is a sectional view summarily representing a crystal-growth process step.

FIG. 11D, a schematic diagram for illustrating the yet even further example of a III-nitride crystal manufacturing method involving the present invention, is an perspective view summarily representing a process step of arranging substrates into a row

FIG. 11E, a schematic diagram for illustrating the yet even further example of a III-nitride crystal manufacturing method involving the present invention, is a sectional view summarily representing a crystal-growth process step.

FIG. 12 is a schematic sectional diagram of a light-emitting ¹⁰ device manufactured utilizing epitaxial III-nitride crystal involving the present invention.

FIG. 13 is a schematic sectional diagram of a light-emitting device manufactured utilizing epitaxial III-nitride crystal involving the present invention.

FIG. 14 is a schematic sectional diagram of an electronic device manufactured utilizing epitaxial III-nitride crystal involving the present invention.

FIG. 15 is an oblique perspective view diagram summarily representing a concrete example of $\{1\text{-}10x\}$ (x: a whole number) planes in a unit cell of a III-nitride crystal that is hexagonal.

FIG. **16** is an oblique perspective view diagram summarily representing a concrete example of {11-2y} (y: a whole number) planes in a unit cell of a III-nitride crystal that is hexagonal.

FIG. 17 is an oblique perspective view diagram summarily representing a concrete example of $\{hk-(h+k)l\}$ (h, k and l: whole numbers) planes in a unit cell of a III-nitride crystal that is hexagonal.

FIG. **18** is a conceptual diagram in which a version of FIG. **4** is modified with a plot of x-ray diffraction FWHMs measured along the [0001] direction of the plural sideways-adjoining III-nitride substrates, indicating correlations, suggested by vertical dotted lines, between the width of the substrates, and pits in III-nitride crystal grown epitaxially on the major surfaces of the substrates, and between the width of the substrates, and intervals between FWHM peak regions.

LEGEND

1: III-nitride bulk crystal

10p, 10q: III-nitride crystal substrates

10pm, 10qm, 20m: major surfaces

10pt, 10qt: adjoining faces

20: III-nitride crystal

20f: facet

20s: direct-over-substrate region

20t: adjoining-substrate supra-region

20*v*: pi

21: III-nitride wafer

90: undersubstrate

91: mask

91w: window

DETAILED DESCRIPTION OF THE INVENTION

In crystallography, in order to represent the plane orientation of crystal faces, notation (Miller notation) such as (hkl) and (hkil) is used. The plane orientation of crystal faces in 60 crystals of the hexagonal crystal system, such as III-nitride crystal, is expressed by (hkil). Herein, h, k, i and l are whole numbers referred to as Miller indices, where the relationship i=–(h+k) holds. The plane of the plane orientation (hkil) is called the (hkil) plane. And the direction perpendicular to the 65 (hkil) plane (the direction of a line normal to the (hkil) plane) is called the [hkil] direction. Meanwhile, "{hkil}" generically

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signifies plane orientations comprehending (hkil) as well as each of its crystallographically equivalent orientations, and "<hkil>" generically signifies directions comprehending [hkil] as well as each of its crystallographically equivalent directions.

One mode of embodying a III-nitride crystal manufacturing method involving the present invention is a method, with reference to FIG. 1, of manufacturing a III-nitride crystal 20 having a major surface 20m of plane orientation $\{h_0k_0i_0l_0\}$ other than {0001}, designated by choice, and includes the following steps. The first step is, as indicated in FIG. 1A, a step of slicing (which will also be termed a "slice-off substrates step" hereinafter) III-nitride bulk crystal 1 into a plurality of III-nitride crystal substrates 10p, 10q having $\{h_0k_0i_0l_0\}$ major surfaces 10pm, 10qm. The second step is, as indicated in FIG. 1B, a step of disposing (which will also be termed a "substrate placement step" hereinafter) the substrates 10p, 10q adjoining each other sideways in such a way that the major surfaces 10pm, 10qm of the plurality of IIInitride crystal substrates 10p, 10q parallel each other and so that the directions in the substrates 10p, 10q are oriented in the same way. The third step is, as indicated in FIG. 1C, a step of growing (which will also be termed a "crystal growth step" hereinafter) III-nitride crystal 20 onto the major surfaces **10**pm, **10**qm of the plurality of III-nitride crystal substrates 10p, 10q.

In the first step (the slice-off substrates step) in the present embodying mode, a plurality of III-nitride crystal substrates $\mathbf{10}p$, $\mathbf{10}q$ having $\{\mathbf{h}_0\mathbf{k}_0\mathbf{i}_0\mathbf{l}_0\}$ major surfaces $\mathbf{10}pm$, $\mathbf{10}qm$ is sliced from III-nitride bulk crystal 1.

The III-nitride bulk crystal 1 utilized in this first step is not particularly limited; crystal manufactured by ordinary methods—that is, growing crystal by HVPE, MOCVD, or other vapor deposition technique, or by flux growth or other liquidphase technique, onto the major surface of, for example, a sapphire substrate having a (0001) major surface or a GaAs substrate having a (111) A-plane major surface—is adequate. Accordingly, although the III-nitride bulk crystal is not par-40 ticularly limited, ordinarily it has a {0001} major surface. Here, from the perspective of diminishing dislocation density and enhancing crystallinity, it is preferable that the III-nitride bulk crystal 1 be grown, as is disclosed in Japanese Unexamined Pat. App. Pub. No. 2001-102307, by a facet growth 45 technique characterized in that facets are formed on the surface where the crystal grows (the crystal growth face), and crystal growth is carried out without filling in the facets.

Likewise, there are no particular limitations on the method whereby the plurality of III-nitride crystal substrates $\mathbf{10p}$, $\mathbf{10q}$ having $\{h_0k_0i_0l_0\}$ major surfaces $\mathbf{10pm}$, $\mathbf{10qm}$ is sliced from the III-nitride bulk crystal 1; as indicated in FIG. 1A, the III-nitride bulk crystal 1 can be cut through a plurality of planes having a predetermined perpendicular spacing along an <hkil> direction (the plane orientation of these planes being $\{hkil\}$, they are also referred to as $\{hkil\}$ planes, as they will be hereinafter).

In the second step (the substrate placement step) in the present embodying mode, as indicated in FIG. 1B, the plural sliced-off III-nitride crystal substrates $\mathbf{10}p$, $\mathbf{10}q$ are disposed adjoining each other sideways in a manner such that the major surfaces $\mathbf{10}pm$, $\mathbf{10}qm$ of the substrates $\mathbf{10}p$, $\mathbf{10}q$ parallel each other and such that the [0001] directions in the substrates $\mathbf{10}p$, $\mathbf{10}q$ are oriented in the same way. In this case, while two adjoining III-nitride crystal substrates $\mathbf{10}p$, $\mathbf{10}q$ among the plurality of III-nitride crystal substrates have been labeled with reference marks in FIG. 1B, the situation is the same with the other adjoining III-nitride crystal substrates.

The plural III-nitride crystal substrates 10p, 10q are disposed sideways so that the major surfaces 10pm, 10qm of the substrates 10p, 10q parallel each other, because if the angles formed by the major surfaces of the substrates and their crystal axes are not uniform along the substrates' major surface, the chemical composition of the III-nitride crystal grown onto the major surfaces of the substrates will be nonuniform along a plane parallel to the substrates' major surface. It is sufficient that the major surfaces 10pm, 10qm of the substrates 10p, 10q be parallel to each other; they need not 10 necessarily be flush with each other. Nevertheless, the difference in height ΔT between the major surfaces 10pm, 10qm of two adjoining III-nitride crystal substrates 10p, 10q is preferably 0.1 mm or less, more preferably 0.01 mm or less.

Furthermore, from the perspective of designing for more 15 uniform crystal growth by making the crystal orientations of the plural III-nitride crystal substrates 10p, 10q the same, the substrates 10p, 10q are disposed sideways in a manner such that the [0001] directions of the substrates 10p, 10q are oriented in the same way. And the plural III-nitride crystal sub- 20 strates 10p, 10q are disposed adjoining each other because if there are gaps between substrates, the crystallinity of crystal that grows over the gaps would be compromised.

Reference is made to FIGS. 1A and 1B: By the first step (the slice-off substrates step) and the second step (the sub- 25 strate placement step), from III-nitride bulk crystal 1 a plurality of III-nitride crystal substrates 10p, 10q having $\{h_0k_0i_0l_0\}$ major surfaces 10pm, 10qm—disposed side-byside in a manner such that the major surfaces 10pm, 10qm of the plurality of III-nitride crystal substrates 10p, 10q are 30 parallel to each other and the [0001] directions in the substrates 10p, 10q are oriented in the same way—is obtained.

In the third step (crystal growth step) in the present embodying mode, III-nitride crystal 20 is grown onto the major surfaces 10pm, 10qm of the plurality of III-nitride 35 crystal substrates 10p, 10q. In this case, growth of the IIInitride crystal 20 is epitaxial growth. Inasmuch as the major surfaces 10pm, 10qm of the plurality of III-nitride crystal substrates 10p, 10q have $\{h_0k_0i_0l_0\}$ plane orientation, the major surface 20m of the III-nitride crystal 20 epitaxially 40 have been abbreviated in the same way. grown onto the major surfaces 10pm, 10qm has the same plane orientation $\{h_0 k_0 i_0 l_0\}$ as the major surfaces 10pm, 10qm of the plurality of III-nitride crystal substrates 10p, 10q. Again, inasmuch as III-nitride crystal 20 is grown onto the major surfaces 10pm, 10qm of the plurality of III-nitride 45 crystal substrates 10p, 10q, the difference in thermal expansion coefficient between the substrates 10p, 10q and the IIInitride crystal 20 that is grown is slight, thanks to which fractures and strains are unlikely to appear in the grown crystal during post-crystal-growth cooling, yielding III-ni- 50 tride crystal of superior crystallinity. From such viewpoints, it is preferable that the plurality of III-nitride crystal substrates 10p, 10q and the III-nitride crystal 20 that is grown be of the same chemical composition. Making that the case enables $\{h_0k_0i_0l_0\}$ major surface 20m, to be manufactured.

In a III-nitride crystal manufacturing method in the present embodying mode, the aforementioned $\{h_0k_0i_0l_0\}$ preferably is any crystallographically equivalent plane orientation selected from the group consisting of $\{1-10x\}$ (wherein x is a 60 whole number), {11-2y} (wherein y is a whole number), and $\{hk-(h+k)l\}$ (wherein h, k and 1 are whole numbers). In III-nitride crystal thus conditioned, a face having any of the plane orientations $\{1-10x\}$, $\{11-2y\}$, or $\{hk-(h+k)l\}$ is a stable face, and therefore, III-nitride crystal of superior crystallinity can be grown stably onto a major surface having such plane orientation.

Again $\{h_0k_0i_0l_0\}$ may be, if not any crystallographically equivalent plane orientation selected from the group consisting of $\{1-10x\}$, $\{11-2y\}$, and $\{hk-(h+k)l\}$, then off-axis by an angle of 5° or less with respect to whichever of these plane orientations. A plane orientation that is misoriented 5° or less with respect to any crystallographically equivalent plane orientation selected from the group consisting of $\{1-10x\}$, $\{11-10x\}$ 2y}, and {hk-(h+k)l} enables crystal growth in the same manner as in $\{1-10x\}$, $\{11-2y\}$, or $\{hk-(h+k)l\}$ instances, and therefore III-nitride crystal of superior crystallinity can be grown stably onto a major surface with such plane orientation. Herein, "off-axis angle" means the angle that any given plane orientation and any other plane orientation form, and is measured by x-ray crystallography.

For reference in the present description, specific examples of $\{1-10x\}$ planes (x: a whole number), $\{11-2y\}$ planes (y: a whole number), and $\{hk-(h+k)l\}$ planes (h, k and l): whole numbers) in a unit cell of III-nitride crystal that is hexagonal are depicted in FIGS. 15 through 17. Herein, the arrows a₁, a₂, a₃ and c are crystal axes for the cell unit of the hexagonal III-nitride crystal.

A face having any crystallographically equivalent plane orientation selected from the group consisting of $\{1-10x\}$, $\{11-2y\}$, and $\{hk-(h+k)l\}$ is in III-nitride crystal a stable face. In the growth of III-nitride crystal, characteristic of the high crystal growth rates afforded by vapor deposition, especially HVPE techniques, is that crystal growth in the c-axis direction (i.e., the [0001] direction) is rapid. Consequently, in III-nitride crystal grown by vapor-phase techniques such as HVPE, the $(1-10\pm1)$ faces, the $(1-10\pm2)$ faces, the $(11-2\pm1)$ faces, the $(11-2\pm 2)$ faces, the $(20-2\pm 1)$ faces and the $(22-4\pm 1)$ faces prove to be more stable. On the other hand, because the crystal growth rate with liquid-phase growth is low, in IIInitride crystal grown by liquid-phase techniques, the $(1-10\pm3)$ faces and the $(11-2\pm3)$ faces prove to be more stable. It will be understood that owing to the cumbersomeness of writing out the notation for both the (10-11) face and the (10-1-1), the notations have been abbreviated as the "(10- 1 ± 1)" faces. The notations for the other plane orientations

In a III-nitride crystal manufacturing method in the present embodying mode, it is preferable that the aforementioned $\{h_0k_0i_0l_0\}$ be any crystallographically equivalent plane orientation selected from the group consisting of {1-100}, {11-20}, $\{1-10\pm2\}$, $\{11-2\pm2\}$, $\{20-2\pm1\}$ and $\{22-4\pm1\}$. Herein, because III-nitride crystal faces whose plane orientations are $\{1-100\}, \{11-20\}, \{1-10\pm2\}, \{11-2\pm2\}, \{20-2\pm1\} \text{ or } \{22-1\}$ 4±1} are stable faces, III-nitride crystal of superior crystallinity can be grown stably onto a major surface with such plane orientation.

Again $\{h_0k_0i_0l_0\}$ may be, rather than any crystallographically equivalent plane orientation selected from the group consisting of $\{1-100\}$, $\{11-20\}$, $\{1-10\pm2\}$, $\{11-2\pm2\}$, $\{20-10\pm2\}$ 2±1} and {22-4±1}, an off-axis angle of 5° or less with III-nitride crystal 20 of superior crystallinity, having a 55 respect to whichever of these plane orientations. A plane orientation that is misoriented 5° or less with respect to any crystallographically equivalent plane orientation selected from the group consisting of $\{1-100\}$, $\{11-20\}$, $\{1-10\pm2\}$, $\{11-2\pm2\}$, $\{20-2\pm1\}$ and $\{22-4\pm1\}$ enables crystal growth in the same manner as in $\{1-100\}$, $\{11-20\}$, $\{1-10\pm2\}$, $\{11-10\pm2\}$ 2 ± 2 , $\{20-2\pm1\}$ and $\{22-4\pm1\}$ instances, and therefore IIInitride crystal of superior crystallinity can be grown stably onto a major surface with such plane orientation.

> It is also preferable in a III-nitride crystal manufacturing method in the present embodying mode that $\{h_0k_0i_0l_0\}$ be {1-100}. In III-nitride crystal, {1-100} is a stable plane and at the same time is a cleavage plane; therefore, III-nitride crystal

of superior crystallinity can be stably grown, and by cleaving the grown III-nitride crystal along the {1-100} planes, IIInitride crystal substrates of superior crystallinity, having a major surface of $\{1-100\}$ plane orientation can be readily produced.

Again $\{h_0k_0i_0l_0\}$ may be, rather than $\{1-100\}$, an off-axis angle of 5° or less with respect to this plane orientation. Being misoriented 5° or less with respect to {1-100} enables crystal growth in the same manner as in implementations on {1-100}, and therefore III-nitride crystal of superior crystallinity can be grown stably onto a major surface with such plane orientation.

Furthermore, in a III-nitride crystal manufacturing method in the present embodying mode, the roughness average Ra of substrates 10p, 10q adjoin each other (here, and likewise below, termed "adjoining faces 10pt, 10qt") preferably is 50 nm or less, more preferably 5 nm or less. If the roughness average Ra of the adjoining faces 10pt, 10qt exceeds 50 nm, the crystallinity of the region 20t in the III-nitride crystal 20 20 above the adjoining faces 10pt, 10qt and their proximity (which will be termed "adjoining-substrate supra-region 20t") will be compromised. Here, "roughness average Ra of a surface" means arithmetic mean roughness Ra, defined in JIS B 0601, and specifically it refers to sampling "cutoff" lengths 25 from a roughness profile along its mean line, and summing, and averaging by cutoff length, distances (the absolute value of deviations) from the mean line to the roughness profile in the sampling section. The surface roughness average Ra can be measured employing an atomic force microscope (AFM) 30 and so on.

In a III-nitride crystal manufacturing method in the present embodying mode, then, in order to have the roughness average Ra of the adjoining faces 10pt, 10qt of the plurality of III-nitride crystal substrates 10p, 10q be 50 nm or less, after 35 the first step (slice-off substrates step) and before the second step (substrate placement step), a step of grinding and/or polishing the edge faces that will become the adjoining faces 10pt, 10qt of the plurality of III-nitride crystal substrates 10p, 10q (also termed grinding/polishing step hereinafter) prefer- 40 ably is included.

Also in a III-nitride crystal manufacturing method in the present embodying mode, from the perspective of further enhancing the crystallinity of the III-nitride crystal that is grown, after the first step (slice-off substrates step) and before 45 the second step (substrate placement step), a step of grinding and/or polishing (grinding/polishing step) the {h₀k₀i₀l₀} major surfaces 10pm, 10qm—which are the surfaces onto which the III-nitride crystal is grown—of the plurality of III-nitride crystal substrates 10p, 10q preferably is included. 50 It is preferable that the surface roughness of the {h₀k₀i₀l₀} major surfaces 10pm, 10qm resulting from such grinding/ polishing step be 50 nm or less; that the roughness be 5 nm is more preferable.

Another advantageous condition in a III-nitride crystal 55 manufacturing method in the present embodying mode is that the temperature at which the III-nitride crystal 20 is grown be 2000° C. or more. This is because with III-nitride crystal grown at a temperature of 2000° C. or more, the crystallinity proves to be uniform globally across the surface where the 60 crystal is grown. Here, "crystallinity is uniform" signifies that the full-width-at-half-maximum in-plane profile of the peak resulting from an x-ray diffraction rocking-curve analysis of the $\{h_0k_0i_0l_0\}$ face is narrow, and that the dislocation-density in-plane distribution as quantified by cathodoluminescence 65 spectroscopy (CL) or etch-pit density (EP) measurement is narrow.

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A still further advantageous condition in a III-nitride crystal manufacturing method in the present embodying mode is that the method whereby the III-nitride crystal 20 is grown be sublimation growth. This is because by sublimation methods, III-nitride crystal is grown at temperatures of 2000° C. or more, and therefore the crystallinity of the III-nitride crystal that is grown proves to be uniform globally across the crystalgrowth surface.

Embodiments

III-Nitride Bulk Crystal Preparation 1

GaN bulk crystal, as III-nitride bulk crystal utilized in a faces 10pt, 10qt along which the plurality of III-nitride crystal 15 III-nitride crystal manufacturing method involving the present invention, was manufactured by a method as below, wherein reference is made to FIG. 2.

> Initially, an SiO₂ layer of 100 nm thickness was formed as a mask layer 91 by sputter deposition onto, as an undersubstrate 90, a GaAs substrate of 50 mm diameter, 0.8 mm thickness, having a (111) A-plane major surface. Next, windows 91w, as illustrated FIGS. 2A and 2B, whose diameter D was 2 µm were formed in the mask by photolithography and etching, arrayed at a pitch P of 4 µm in a close-hexagonal packing pattern. In this case the GaAs substrate 90 was exposed through the windows 91w.

> Next, GaN bulk crystal, as III-nitride bulk crystal, was grown by HVPE onto the GaAs substrate 90 on which the mask 91 having a plurality of the windows 91w had been formed. Specifically, a GaN low-temperature layer of 80 nm thickness was grown by HVPE at 500° C. onto the GaAs substrate, onto that layer subsequently a GaN intermediate layer of 60 µm thickness was grown at 950° C., after which GaN bulk crystal of 5 mm thickness was grown at 1050° C. onto the intermediate layer.

> Next, the GaAs substrate was removed from the GaN bulk crystal by an etching process employing aqua regia, to yield GaN bulk crystal of 50 mm diameter and 3 mm thickness, as III-nitride bulk crystal.

Embodiment 1

To begin with, referring to FIG. 3A, the (0001) face and (000-1) face—the two major surfaces—of GaN bulk crystal (III-nitride bulk crystal 1) were ground and polished to bring the roughness average Ra of either major surface to 5 nm. Here measurement of the surface roughness average Ra was carried out by AFM.

Next, referring to FIG. 3A, the GaN bulk crystal (III-nitride bulk crystal 1) whose roughness average Ra on either of its major surfaces had been made 5 nm was sawed along a plurality of planes perpendicular to a <1-100> direction to slice off a plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) whose width S was 3 mm, length L was 20 to 50 mm, and thickness T was 1 mm, having {1-100} major surfaces. Subsequently, the not-ground and not-polished four sides of each sliced-off GaN crystal substrate were ground and polished, to bring the roughness average Ra of the four surfaces to 5 nm. A plurality of GaN crystal substrates whose roughness average Ra on the {1-100} major surfaces was 5 nm was thus obtained. Among these GaN crystal substrates were GaN crystal substrates whose majorsurface plane orientation did not coincide perfectly with {1-100}, but the plane orientation of the major surface of such GaN crystal substrates in all cases was misoriented by 5° or less with respect to {1-100}. Here, the off-axis angle was measured by x-ray crystallography.

Next, referring to FIG. 3B, these GaN crystal substrates were disposed adjoining each other sideways, in a manner such that the (1-100) major surfaces 10pm, 10qm of the plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) would be parallel to each other, and such that the [0001] directions in the GaN crystal substrates (III-nitride crystal substrates 10p, 10q) would be oriented in the same way. In this instance, referring also to FIG. 3C, the roughness average Ra of the adjoining faces 10pt, 10qt of the plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) was 5 nm.

Next, with reference again to FIG. 3C, the (1-100) major surfaces 10pm, 10qm of the situated plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) were processed for two hours at 800° C. under an atmosphere that was a gaseous mixture of 10 volume % hydrogen chloride and 90 volume % nitrogen, and afterwards, by HVPE and at a crystal growth temperature of 1050° C., GaN crystal (epitaxial III-nitride crystal 20) was grown onto the major surfaces 10pm, 10qm for 50 hours at a deposition rate of 20 µm/hr, yielding a III-nitride crystal composite.

The obtained GaN crystal (epitaxial III-nitride crystal 20), free of abnormal growth even in the adjoining-substrate supra-regions 20t, had a (1-100) major surface 20m. The crystallinity of the GaN crystal (epitaxial III-nitride crystal 20) was characterized by an x-ray diffraction rocking-curve analysis of the (1-100) plane. With this GaN crystal, the direct-over-substrate regions 20s (meaning—likewise hereinafter—the regions 20s that are directly above the plural III-nitride crystal substrates 10p, 10q), demonstrated diffraction peaks undivided in the tip, with the full-width at halfmaximum being 100 arcsec. In the adjoining-substrate supraregions 20t, meanwhile, diffraction peaks with divisions in the tip were demonstrated, with the full-width at half-maximum (FWHM) being 300 arcsec. Also, with the x-ray diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto the (1-100) 35 plane, FWHM peak regions were present at intervals of almost 3 mm width, as indicated in FIG. 18.

It should be understood that a "principal impurity(ies)" as given in the present specification is an impurity(ies) that is present in an amount occupying at least 95% among the 40 entirety of impurities in the III-nitride crystal. Saying "the principal impurities are oxygen atoms and silicon atoms" means "oxygen atoms (O) and silicon atoms (Si) occupy at least 95% among the entirety of the impurities in the III-nitride crystal."

Furthermore, threading dislocation density in the (1-100) major surface 20m of the GaN crystal was determined by cathodoluminescence spectroscopy (termed "CL" hereinafter), whereupon the density was 1×10^7 cm⁻² in the direct-over-substrate regions 20s, and 3×10^7 cm⁻² in the adjoining-substrate supra-regions 20t. In addition, the carrier concentration in the GaN crystal was determined by Hall-effect measurements, whereupon it was 5×10^{18} cm⁻³. And the principal impurities in the GaN crystal according to SIMS (secondary ion mass spectrometry, likewise hereinafter) were oxygen (O) atoms and silicon (Si) atoms. The results are 55 tabulated in Table I.

It should be understood that in Embodiment 1, the major-surface plane orientations of the plurality of GaN crystal substrates that were the surfaces onto which the GaN crystal was grown were all (1-100), in that although a few or more 60 might have been (-1100), the crystallographic equivalent of (1-100), they would lead to the same results.

Embodiment 2

Reference is again made to FIG. 3A: Apart from grinding and polishing the (0001) face and (000-1) face—the two

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major surfaces—of GaN bulk crystal (III-nitride bulk crystal 1) to bring the roughness average Ra of either major surface to 50 nm, in the same manner as in Embodiment 1, plural GaN crystal substrates (III-nitride crystal substrates 10p, 10q) were sliced off, and the not-ground and not-polished four sides of each GaN crystal substrate were ground and polished, to bring the roughness average Ra of the four surfaces to 5 nm. Among the plurality of GaN crystal substrates were GaN crystal substrates whose major-surface plane orientation did not coincide perfectly with $\{1\text{-}100\}$, but the plane orientation of the major surface of such GaN crystal substrates in all cases was misoriented by 5° or less with respect to $\{1\text{-}100\}$.

Next, referring to FIG. 3B, the plural GaN crystal substrates (III-nitride crystal substrates 10p, 10q) were situated in the same manner as in Embodiment 1. In this case, referring also to FIG. 4, the roughness average Ra of the adjoining faces 10pt, 10qt of the plurality of GaN crystal substrates was 50 nm

Next, with reference again to FIG. 4: The (1-100) major surfaces 10pm, 10qm of the situated plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) were processed in the same way as that of Embodiment 1, and thereafter GaN crystal (III-nitride crystal 20) was grown onto the major surfaces 10pm, 10qm under the same conditions as those of Embodiment 1.

The obtained GaN crystal (III-nitride crystal **20**) had a (1-100) major surface **20***m* in which pits **20***v* constituted by a plurality of facets **20***f* were formed in the adjoining-substrate supra-regions **20***t*. Further, in an x-ray diffraction rocking-curve analysis of the (1-100) plane of the GaN crystal (III-nitride crystal **20**), the direct-over-substrate regions **20***s* demonstrated diffraction peaks undivided in the tip, with the full-width at half-maximum being 100 arcsec. In the adjoining-substrate supra-regions **20***t*, meanwhile, diffraction peaks with divisions in the tip were demonstrated, with the full-width at half-maximum being 800 arcsec. Also, with the x-ray diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto the (1-100) plane, FWHM peak regions were present at intervals of almost 3 mm width, as indicated in FIG. **18**.

Furthermore, threading dislocation density in the (1-100) major surface 20m of the GaN crystal was 1×10^7 cm⁻² in the direct-over-substrate regions 20s, and 8×10^7 cm⁻² in the adjoining-substrate supra-regions 20t. In addition, the carrier concentration in the GaN crystal was 5×10^{18} cm⁻³, while the principal impurities were oxygen atoms and silicon atoms. The results are tabulated in Table I.

It should be understood that in Embodiment 2, the majorsurface plane orientations of the plurality of GaN crystal substrates that were the surfaces onto which the GaN crystal was grown were all (1-100), in that although a few or more might have been (-1100), the crystallographic equivalent of (1-100), they would lead to the same results.

Embodiment 3

To begin with, referring to FIG. **5**A, the (0001) face and (000-1) face—the two major surfaces—of GaN bulk crystal (III-nitride bulk crystal 1) were ground and polished to bring the roughness average Ra of either major surface to 5 nm.

Next, again referring to FIG. **5**A: The GaN bulk crystal (III-nitride bulk crystal **1**) whose roughness average Ra on either of its major surfaces had been made 5 nm was sawed along a plurality of planes perpendicular to a <11-20> direction to slice off a plurality of GaN crystal substrates (III-nitride crystal substrates **10**p, **10**q) whose width S was 3 mm, length L was 20 to 50 mm, and thickness T was 1 mm, having

{11-20} major surfaces. Subsequently, the not-ground and not-polished four sides of each sliced-off GaN crystal substrate were ground and polished, to bring the roughness average Ra of the four surfaces to 5 nm. A plurality of GaN crystal substrates whose roughness average Ra on the {11-20} major 5 surfaces was 5 nm was thus obtained. Among these GaN crystal substrates were GaN crystal substrates whose major-surface plane orientation did not coincide perfectly with {11-20}, but the plane orientation of the major surface of such GaN crystal substrates in all cases was misoriented by 5° or 10 less with respect to {11-20}.

Next, referring to FIG. **5**B, these GaN crystal substrates were disposed adjoining each other sideways, in a manner such that the (11-20) major surfaces **10**pm, **10**qm of the plurality of GaN crystal substrates (III-nitride crystal substrates 15 **10**p, **10**q) would be parallel to each other, and such that the [0001] directions in the GaN crystal substrates would be oriented in the same way. In this instance, referring also to FIG. **5**C, the roughness average Ra of the adjoining faces **10**pt, **10**qt of the plurality of GaN crystal substrates was 5 nm. ²⁰

Next, with reference again to FIG. **5**C: The (11-20) major surfaces **10***pm*, **10***qm* of the situated plurality of GaN crystal substrates (III-nitride crystal substrates **10***p*, **10***q*) were processed in the same way as that of Embodiment 1, and thereafter GaN crystal (III-nitride crystal **20**) was grown onto the ²⁵ major surfaces **10***pm*, **10***qm* under the same conditions as those of Embodiment 1.

The obtained GaN crystal (III-nitride crystal **20**) had a (11-20) major surface **20***m* in which pits **20***v* defined by a plurality of facets **20***f* were formed in the adjoining-substrate supra-regions **20***t*. Further, in an x-ray diffraction rocking-curve analysis of the (11-20) plane of the GaN crystal (III-nitride crystal **20**), the direct-over-substrate regions **20***s* demonstrated diffraction peaks undivided in the tip, with the full-width at half-maximum being 250 arcsec. In the adjoining-substrate supra-regions **20***t*, meanwhile, diffraction peaks with divisions in the tip were demonstrated, with the full-width at half-maximum being 620 arcsec. Also, with the x-ray diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto the (11-20) 40 plane, FWHM peak regions were present at intervals of almost 3 mm width, as indicated in FIG. **18**.

Furthermore, threading dislocation density in the (11-20) major surface 20m of the GaN crystal was 1×10^7 cm⁻² in the direct-over-substrate regions 20s, and 8×10^7 cm⁻² in the 45 adjoining-substrate supra-regions 20t. In addition, the carrier concentration in the GaN crystal was 5×10^{18} cm⁻³, while the principal impurities were oxygen atoms and silicon atoms. The results are tabulated in Table I.

It should be understood that in Embodiment 3, the major-surface plane orientations of the plurality of GaN crystal substrates that were the surfaces onto which the GaN crystal was grown were all (11-20), in that although a few or more might have been (-1-120), the crystallographic equivalent of (11-20), they would lead to the same results.

Embodiment 4

To begin with, referring to FIG. 6A, the (0001) face and (000-1) face—the two major surfaces—of GaN bulk crystal 60 (III-nitride bulk crystal 1) were ground to bring the roughness average Ra of either major surface to 50 nm.

Next, again referring to FIG. **6**A: The GaN bulk crystal (III-nitride bulk crystal **1**) whose roughness average Ra on either of its major surfaces had been made 50 nm was sawed 65 along a plurality of planes perpendicular to a <1-102> direction to slice off a plurality of GaN crystal substrates (III-

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nitride crystal substrates 10p, 10q) whose width S was 5 mm, length L was 20 to 50 mm, and thickness T was 1 mm, having $\{1\text{-}102\}$ major surfaces. Subsequently, the six sides of each sliced-off GaN crystal substrate were ground and polished, to bring the surfaces' roughness average Ra to 5 nm. A plurality of GaN crystal substrates whose roughness average Ra on the $\{1\text{-}102\}$ major surfaces was 5 nm was thus obtained. Among these GaN crystal substrates were GaN crystal substrates whose major-surface plane orientation did not coincide perfectly with $\{1\text{-}102\}$, but the plane orientation of the major surface of such GaN crystal substrates in all cases was misoriented by 5° or less with respect to $\{1\text{-}102\}$.

Next, referring to FIG. 6B, these GaN crystal substrates were disposed adjoining each other sideways, in a manner such that the (1-102) major surfaces 10pm, 10qm of the plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) would be parallel to each other, and such that the [0001] directions in the GaN crystal substrates would be oriented in the same way. In this instance, referring also to FIG. 6C, the roughness average Ra of the adjoining faces 10pt, 10qt of the plurality of GaN crystal substrates was 5 nm.

Next, with reference again to FIG. 6C: The (1-102) major surfaces 10pm, 10qm of the situated plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) were processed in the same way as that of Embodiment 1, and thereafter GaN crystal (III-nitride crystal 20) was grown onto the major surfaces 10pm, 10qm under the same conditions as those of Embodiment 1.

The obtained GaN crystal, free of abnormal growth even in the adjoining-substrate supra-regions 20t, had a (1-102) major surface 20m. In an x-ray diffraction rocking-curve analysis of the (1-102) plane of the GaN crystal (III-nitride crystal 20), the direct-over-substrate regions 20s demonstrated diffraction peaks undivided in the tip, with the full-width at half-maximum being 120 arcsec. In the adjoining-substrate supra-regions 20t, meanwhile, diffraction peaks with divisions in the tip were demonstrated, with the full-width at half-maximum being 480 arcsec. Also, with the x-ray diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto the (1-102) plane, FWHM peak regions were present at intervals of almost 5 mm width, as indicated in FIG. 18.

Furthermore, threading dislocation density in the (1-102) major surface 20m of the GaN crystal was 1×10^7 cm⁻² in the direct-over-substrate regions 20s, and 6×10^7 cm⁻² in the adjoining-substrate supra-regions 20t. In addition, the carrier concentration in the GaN crystal was 5×10^{18} cm⁻³, while the principal impurities were oxygen atoms and silicon atoms. The results are tabulated in Table I.

It should be understood that in Embodiment 4, the major-surface plane orientations of the plurality of GaN crystal substrates that were the surfaces onto which the GaN crystal was grown were all (1-102), in that although a few or more might have been (-1102), the crystallographic equivalent of
 (1-102), they would lead to the same results.

Embodiment 5

To begin with, referring to FIG. **6**A, the (0001) face and (000-1) face—the two major surfaces—of GaN bulk crystal (III-nitride bulk crystal **1**) were ground to bring the roughness average Ra of either major surface to 50 nm.

Next, again referring to FIG. **6**A: The GaN bulk crystal (III-nitride bulk crystal **1**) whose roughness average Ra on either of its major surfaces had been made 50 nm was sawed along a plurality of planes perpendicular to a <1-10-2> direction to slice off a plurality of GaN crystal substrates (III-

nitride crystal substrates 10p, 10q) whose width S was 5 mm, length L was 20 to 50 mm, and thickness T was 1 mm, having $\{1\text{-}10\text{-}2\}$ major surfaces. Subsequently, the six sides of each sliced-off GaN crystal substrate were ground and polished, to bring the surfaces' roughness average Ra to 5 nm. A plurality of GaN crystal substrates whose roughness average Ra on the $\{1\text{-}10\text{-}2\}$ major surfaces was 5 nm was thus obtained. Among these GaN crystal substrates were GaN crystal substrates whose major-surface plane orientation did not coincide perfectly with $\{1\text{-}10\text{-}2\}$, but the plane orientation of the major surface of such GaN crystal substrates in all cases was misoriented by 5° or less with respect to $\{1\text{-}10\text{-}2\}$.

Next, referring to FIG. 6D, these GaN crystal substrates were disposed adjoining each other sideways, in a manner such that the (1-10-2) major surfaces 10pm, 10qm of the 15 plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) would be parallel to each other, and such that the [0001] directions in the GaN crystal substrates would be oriented in the same way. In this instance, referring also to FIG. 6C, the roughness average Ra of the adjoining faces 20 10pt, 10qt of the plurality of GaN crystal substrates was 5 nm.

With reference now to FIG. 6E: Next, the (1-10-2) major surfaces 10pm, 10qm of the situated plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) were processed in the same way as that of Embodiment 1, and thereafter GaN crystal (III-nitride crystal 20) was grown onto the major surfaces 10pm, 10qm under the same conditions as those of Embodiment 1.

The obtained GaN crystal, free of abnormal growth even in the adjoining-substrate supra-regions 20t, had a (1-10-2) 30 major surface 20m. In an x-ray diffraction rocking-curve analysis of the (1-10-2) plane of the GaN crystal (III-nitride crystal 20), the direct-over-substrate regions 20s demonstrated diffraction peaks undivided in the tip, with the full-width at half-maximum being 100 arcsec. In the adjoining-substrate supra-regions 20t, meanwhile, diffraction peaks with divisions in the tip were demonstrated, with the full-width at half-maximum being 420 arcsec. Also, with the x-ray diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto the (1-10-40 2) plane, FWHM peak regions were present at intervals of almost 5 mm width, as indicated in FIG. 18.

Furthermore, threading dislocation density in the (1-10-2) major surface 20m of the GaN crystal was 1×10^7 cm⁻² in the direct-over-substrate regions 20s, and 6×10^7 cm⁻² in the 45 adjoining-substrate supra-regions 20t. In addition, the carrier concentration in the GaN crystal was 5×10^{18} cm⁻³, while the principal impurities were oxygen atoms and silicon atoms. The results are tabulated in Table I.

It should be understood that in Embodiment 5, the major-surface plane orientations of the plurality of GaN crystal substrates that were the surfaces onto which the GaN crystal was grown were all (1-10-2), in that although a few or more might have been (-110-2), the crystallographic equivalent of (1-10-2), they would lead to the same results.

Embodiment 6

To begin with, referring to FIG. 7A, the (0001) face and (000-1) face—the two major surfaces—of GaN bulk crystal 60 (III-nitride bulk crystal 1) were ground to bring the roughness average Ra of either major surface to 50 nm.

Next, again referring to FIG. 7A: The GaN bulk crystal (III-nitride bulk crystal 1) whose roughness average Ra on either of its major surfaces had been made 50 nm was sawed 65 along a plurality of planes perpendicular to a <11-22> direction to slice off a plurality of GaN crystal substrates (III-

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nitride crystal substrates 10p, 10q) whose width S was 5 mm, length L was 20 to 50 mm, and thickness T was 1 mm, having $\{11\text{-}22\}$ major surfaces. Subsequently, the six sides of each sliced-off GaN crystal substrate were ground and polished, to bring the roughness average Ra of the six surfaces to 5 nm. A plurality of GaN crystal substrates whose roughness average Ra on the $\{11\text{-}22\}$ major surfaces was 5 nm was thus obtained. Among these GaN crystal substrates were GaN crystal substrates whose major-surface plane orientation did not coincide perfectly with $\{11\text{-}22\}$, but the plane orientation of the major surface of such GaN crystal substrates in all cases was misoriented by 5° or less with respect to $\{11\text{-}22\}$.

Next, referring to FIG. 7B, these GaN crystal substrates were disposed adjoining each other sideways, in a manner such that the (11-22) major surfaces 10pm, 10qm of the plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) would be parallel to each other, and such that the [0001] directions in the GaN crystal substrates would be oriented in the same way. In this instance, the roughness average Ra of the adjoining faces 10pt, 10qt of the plurality of GaN crystal substrates was 5 nm.

Next, with reference to FIG. 7C: The (11-22) major surfaces 10pm, 10qm of the situated plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) were processed in the same way as that of Embodiment 1, and thereafter GaN crystal (III-nitride crystal 20) was grown onto the major surfaces 10pm, 10qm under the same conditions as those of Embodiment 1.

The obtained GaN crystal, free of abnormal growth even in the adjoining-substrate supra-regions 20t, had a (11-22) major surface 20m. In an x-ray diffraction rocking-curve analysis of the (11-22) plane of the GaN crystal (III-nitride crystal 20), the direct-over-substrate regions 20s demonstrated diffraction peaks undivided in the tip, with the full-width at half-maximum being 90 arcsec. In the adjoining-substrate supra-regions 20t, meanwhile, diffraction peaks with divisions in the tip were demonstrated, with the full-width at half-maximum being 380 arcsec. Also, with the x-ray diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto the (11-22) plane, FWHM peak regions were present at intervals of almost 5 mm width, as indicated in FIG. 18.

Furthermore, threading dislocation density in the (11-22) major surface 20m of the GaN crystal was 1×10^7 cm⁻² in the direct-over-substrate regions 20s, and 4×10^7 cm⁻² in the adjoining-substrate supra-regions 20t. In addition, the carrier concentration in the GaN crystal was 5×10^{18} cm⁻³, while the principal impurities were oxygen atoms and silicon atoms. The results are tabulated in Table I.

50 It should be understood that in Embodiment 6, the major-surface plane orientations of the plurality of GaN crystal substrates that were the surfaces onto which the GaN crystal was grown were all (11-22), in that although a few or more might have been (-2112), the crystallographic equivalent of 55 (11-22), they would lead to the same results.

Embodiment 7

To begin with, referring to FIG. 7A, the (0001) face and (000-1) face—the two major surfaces—of GaN bulk crystal (III-nitride bulk crystal 1) were ground to bring the roughness average Ra of either major surface to 50 nm.

Next, again referring to FIG. 7A: The GaN bulk crystal (III-nitride bulk crystal 1) whose roughness average Ra on either of its major surfaces had been made 50 nm was sawed along a plurality of planes perpendicular to a <11-2-2> direction to slice off a plurality of GaN crystal substrates (III-

nitride crystal substrates $\mathbf{10p}$, $\mathbf{10q}$) whose width S was 5 mm, length L was 20 to 50 mm, and thickness T was 1 mm, having $\{11\text{-}2\text{-}2\}$ major surfaces. Subsequently, the six sides of each sliced-off GaN crystal substrate were ground and polished, to bring the roughness average Ra of the six surfaces to 5 nm. A 5 plurality of GaN crystal substrates whose roughness average Ra on the $\{11\text{-}2\text{-}2\}$ major surfaces was 5 nm was thus obtained. Among these GaN crystal substrates were GaN crystal substrates whose major-surface plane orientation did not coincide perfectly with $\{11\text{-}2\text{-}2\}$, but the plane orientation of the major surface of such GaN crystal substrates in all cases was misoriented by 5° or less with respect to $\{11\text{-}2\text{-}2\}$.

Next, referring to FIG. 7D, these GaN crystal substrates were disposed adjoining each other sideways, in a manner such that the (11-2-2) major surfaces 10pm, 10qm of the ¹⁵ plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) would be parallel to each other, and such that the [0001] directions in the GaN crystal substrates would be oriented in the same way. In this instance, the roughness average Ra of the adjoining faces 10pt, 10qt of the plurality of ²⁰ GaN crystal substrates was 5 nm.

With reference now to FIG. 7E: Next, the (11-2-2) major surfaces 10pm, 10qm of the situated plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) were processed in the same way as that of Embodiment 1, and thereafter GaN crystal (III-nitride crystal 20) was grown onto the major surfaces 10pm, 10qm under the same conditions as those of Embodiment 1.

The obtained GaN crystal, free of abnormal growth even in the adjoining-substrate supra-regions 20t, had a (11-2-2) 30 major surface 20m. In an x-ray diffraction rocking-curve analysis of the (11-2-2) plane of the GaN crystal (III-nitride crystal 20), the direct-over-substrate regions 20s demonstrated diffraction peaks undivided in the tip, with the full-width at half-maximum being 80 arcsec. In the adjoining-substrate supra-regions 20t, meanwhile, diffraction peaks with divisions in the tip were demonstrated, with the full-width at half-maximum being 360 arcsec. Also, with the x-ray diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto the (11-2-2) plane, FWHM peak regions were present at intervals of almost 5 mm width, as indicated in FIG. 18.

Furthermore, threading dislocation density in the (11-2-2) major surface 20m of the GaN crystal was 1×10^7 cm⁻² in the direct-over-substrate regions 20s, and 4×10^7 cm⁻² in the 45 adjoining-substrate supra-regions 20t. In addition, the carrier concentration in the GaN crystal was 5×10^{18} cm⁻³, while the principal impurities were oxygen atoms and silicon atoms. The results are tabulated in Table I.

It should be understood that in Embodiment 7, the major-surface plane orientations of the plurality of GaN crystal substrates that were the surfaces onto which the GaN crystal was grown were all (11-2-2), in that although a few or more might have been (-211-2), the crystallographic equivalent of (11-2-2), they would lead to the same results.

Embodiment 8

To begin with, referring to FIG. **8**A, the (0001) face and (000-1) face—the two major surfaces—of GaN bulk crystal 60 (III-nitride bulk crystal 1) were ground and polished to bring the roughness average Ra of either major surface to 5 nm.

Next, again referring to FIG. **8**A: The GaN bulk crystal (III-nitride bulk crystal **1**) whose roughness average Ra on either of its major surfaces had been made 5 nm was sawed 65 along a plurality of planes perpendicular to a <12-30> direction to slice off a plurality of GaN crystal substrates (III-

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nitride crystal substrates 10p, 10q) whose width S was 3 mm, length L was 20 to 50 mm, and thickness T was 1 mm, having {12-30} major surfaces. Subsequently, the not-ground and not-polished four sides of each sliced-off GaN crystal substrate were ground and polished, to bring the roughness average Ra of the four surfaces to 5 nm. A plurality of GaN crystal substrates whose roughness average Ra on the {12-30} major surfaces was 5 nm was thus obtained. Among these GaN crystal substrates were GaN crystal substrates whose major-surface plane orientation did not coincide perfectly with {12-30}, but the plane orientation of the major surface of such GaN crystal substrates in all cases was misoriented by 5° or less with respect to {12-30}.

Next, referring to FIG. **8**B, these GaN crystal substrates were disposed adjoining each other sideways, in a manner such that the (12-30) major surfaces 10pm, 10qm of the plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) would be parallel to each other, and such that the [0001] directions in the GaN crystal substrates would be oriented in the same way. In this instance, referring also to FIG. **8**C, the roughness average Ra of the adjoining faces 10pt, 10qt of the plurality of GaN crystal substrates was 5 nm.

Next, with reference again to FIG. 8C: The (12-30) major surfaces 10pm, 10qm of the situated plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) were processed in the same way as that of Embodiment 1, and thereafter GaN crystal (III-nitride crystal 20) was grown onto the major surfaces 10pm, 10qm under the same conditions as those of Embodiment 1.

The obtained GaN crystal (III-nitride crystal **20**) had a (12-30) major surface **20**m in which pits **20**v constituted by a plurality of facets **20**f were formed in the adjoining-substrate supra-regions **20**t. Further, in an x-ray diffraction rocking-curve analysis of the (12-30) plane of the GaN crystal (III-nitride crystal **20**), the direct-over-substrate regions **20**s demonstrated diffraction peaks undivided in the tip, with the full-width at half-maximum being 280 arcsec. In the adjoining-substrate supra-regions **20**t, meanwhile, diffraction peaks with divisions in the tip were demonstrated, with the full-width at half-maximum being 660 arcsec. Also, with the x-ray diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto the (12-30) plane, FWHM peak regions were present at intervals of almost 3 mm width, as indicated in FIG. **18**.

Furthermore, threading dislocation density in the (12-30) major surface 20m of the GaN crystal was 1×10^7 cm⁻² in the direct-over-substrate regions 20s, and 7×10^7 cm⁻² in the adjoining-substrate supra-regions 20t. In addition, the carrier concentration in the GaN crystal was 4×10^{18} cm⁻³, while the principal impurities were oxygen atoms and silicon atoms. The results are tabulated in Table I.

It should be understood that in Embodiment 8, the majorsurface plane orientations of the plurality of GaN crystal substrates that were the surfaces onto which the GaN crystal 55 was grown were all (12-30), in that although a few or more might have been (-3210), the crystallographic equivalent of (12-30), they would lead to the same results.

Embodiment 9

To begin with, referring to FIG. 9A, the (0001) face and (000-1) face—the two major surfaces—of GaN bulk crystal (III-nitride bulk crystal 1) were ground and polished to bring the roughness average Ra of either major surface to 5 nm.

Next, again referring to FIG. 9A: The GaN bulk crystal (III-nitride bulk crystal 1) whose roughness average Ra on either of its major surfaces had been made 5 nm was sawed

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along a plurality of planes perpendicular to a <23-50> direction to slice off a plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) whose width S was 3 mm, length L was 20 to 50 mm, and thickness T was 1 mm, having {23-50} major surfaces. Subsequently, the not-ground and not-polished four sides of each sliced-off GaN crystal substrate were ground and polished, to bring the roughness average Ra of the four surfaces to 5 mm. A plurality of GaN crystal substrates whose roughness average Ra on the {23-50} major surfaces was 5 mm was thus obtained. Among these GaN crystal substrates were GaN crystal substrates whose major-surface plane orientation did not coincide perfectly with {23-50}, but the plane orientation of the major surface of such GaN crystal substrates in all cases was misoriented by 5° or less with respect to {23-50}.

Next, referring to FIG. 9B, these GaN crystal substrates were disposed adjoining each other sideways, in a manner such that the (23-50) major surfaces 10pm, 10qm of the plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) would be parallel to each other, and such that the [0001] directions in the GaN crystal substrates would be 20 oriented in the same way. In this instance, the roughness average Ra of the adjoining faces 10pt, 10qt of the plurality of GaN crystal substrates was 5 nm.

With reference now to FIG. 9C: Next, GaN crystal (III-nitride crystal 20) was deposited by flux growth onto the 25 (23-50) major surfaces 10pm, 10qm of the situated plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q). Specifically, a Ga—Na melt (a liquefied mixture of Ga and Na) was contacted on the (23-50) major surfaces 10pm, 10qm of the plurality of GaN crystal substrates, and under crystal-growth temperature and crystal-growth pressure (gaseous-nitrogen pressure) conditions of 870° C. and 4 MPa (40 atmospheres), GaN crystal (III-nitride crystal 20) was grown onto the (23-50) major surfaces 10pm, 10qm of the GaN crystal substrates for 100 hours at a deposition rate of 5 µm/hr.

The obtained GaN crystal, free of abnormal growth even in the adjoining-substrate supra-regions 20t, had a (23-50) major surface 20m. In an x-ray diffraction rocking-curve analysis of the (23-50) plane of the GaN crystal (III-nitride crystal 20), the direct-over-substrate regions 20s demonstrated diffraction peaks undivided in the tip, with the full-width at half-maximum being 230 arcsec. In the adjoining-substrate supra-regions 20t, meanwhile, diffraction peaks with divisions in the tip were demonstrated, with the full-width at half-maximum being 490 arcsec. Also, with the x-ray 45 diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto the (23-50) plane, FWHM peak regions were present at intervals of almost 3 mm width, as indicated in FIG. 18.

Furthermore, threading dislocation density in the (23-50) 50 major surface 20m of the GaN crystal was 1×10^7 cm⁻² in the direct-over-substrate regions 20s, and 4×10^7 cm⁻² in the adjoining-substrate supra-regions 20t. In addition, the carrier concentration in the GaN crystal was 3×10^{18} cm⁻³, while the principal impurities were oxygen atoms and silicon atoms. 55 The results are tabulated in Table I.

It should be understood that in Embodiment 9, the plane orientations of the major surfaces 10pm, 10qm of the plurality of GaN crystal substrates that were the surfaces onto which the GaN crystal was grown were all (23-50), in that although 60 a few or more might have been (-5230), the crystallographic equivalent of (23-50), they would lead to the same results.

Embodiment 10

To begin with, referring to FIG. **10**A, the (0001) face and (000-1) face—the two major surfaces—of GaN bulk crystal

(III-nitride bulk crystal 1) were ground and polished to bring the roughness average Ra of either major surface to 5 nm.

Next, again referring to FIG. 10A: The GaN bulk crystal (III-nitride bulk crystal 1) whose roughness average Ra on either of its major surfaces had been made 5 nm was sawed along a plurality of planes perpendicular to a <20-21> direction to slice off a plurality of GaN crystal substrates (IIInitride crystal substrates 10p, 10q) whose width S was 3 mm, length L was 20 to 50 mm, and thickness T was 1 mm, having {20-21} major surfaces. Subsequently, the not-ground and not-polished four sides of each sliced-off GaN crystal substrate were ground and polished, to bring the roughness average Ra of the four surfaces to 5 nm. A plurality of GaN crystal substrates whose roughness average Ra on the {20-21} major surfaces was 5 nm was thus obtained. Among these GaN crystal substrates were GaN crystal substrates whose majorsurface plane orientation did not coincide perfectly with {20-21}, but the plane orientation of the major surface of such GaN crystal substrates in all cases was misoriented by 5° or less with respect to {20-21}

Next, referring to FIG. 10B, these GaN crystal substrates were disposed adjoining each other sideways, in a manner such that the (20-21) major surfaces 10pm, 10qm of the plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) would be parallel to each other, and such that the [0001] directions in the GaN crystal substrates would be oriented in the same way. In this instance, referring also to FIG. 10C, the roughness average Ra of the adjoining faces 10pt, 10qt of the plurality of GaN crystal substrates was 5 nm.

Next, with reference again to FIG. 10C: The (20-21) major surfaces 10pm, 10qm of the situated plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) were processed in the same way as that of Embodiment 1, and thereafter GaN crystal (III-nitride crystal 20) was grown onto the major surfaces 10pm, 10qm under the same conditions as those of Embodiment 1.

The obtained GaN crystal, free of abnormal growth even in the adjoining-substrate supra-regions 20t, had a (20-21) major surface 20m. In an x-ray diffraction rocking-curve analysis of the (20-21) plane of the GaN crystal (III-nitride crystal 20), the direct-over-substrate regions 20s demonstrated diffraction peaks undivided in the tip, with the full-width at half-maximum being 120 arcsec. In the adjoining-substrate supra-regions 20t, meanwhile, diffraction peaks that with divisions in the tip were demonstrated, with the full-width at half-maximum being 380 arcsec. Also, with the x-ray diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto the (20-21) plane, FWHM peak regions were present at intervals of almost 3 mm width, as indicated in FIG. 18.

Furthermore, threading dislocation density in the (20-21) major surface 20m of the GaN crystal was 1×10^7 cm⁻² in the direct-over-substrate regions 20s, and 4×10^7 cm⁻² in the adjoining-substrate supra-regions 20t. In addition, the carrier concentration in the GaN crystal was 1×10^{18} cm⁻³, while the principal impurities were oxygen atoms and silicon atoms. The results are tabulated in Table I.

It should be understood that in Embodiment 10, the majorsurface plane orientations of the plurality of GaN crystal substrates that were the surfaces onto which the GaN crystal was grown were all (20-21), in that although a few or more might have been (-2021), the crystallographic equivalent of (20-21), they would lead to the same results.

Embodiment 11

To begin with, referring to FIG. **10**A, the (0001) face and (000-1) face—the two major surfaces—of GaN bulk crystal

(III-nitride bulk crystal 1) were ground and polished to bring the roughness average Ra of either major surface to 5 nm.

Next, again referring to FIG. 10A: The GaN bulk crystal (III-nitride bulk crystal 1) whose roughness average Ra on either of its major surfaces had been made 5 nm was sawed along a plurality of planes perpendicular to a <20-2-1> direction to slice off a plurality of GaN crystal substrates (IIInitride crystal substrates 10p, 10q) whose width S was 3 mm, length L was 20 to 50 mm, and thickness T was 1 mm, having {20-2-1} major surfaces. Subsequently, the not-ground and not-polished four sides of each sliced-off GaN crystal substrate were ground and polished, to bring the roughness average Ra of the four surfaces to 5 nm. A plurality of GaN crystal substrates whose roughness average Ra on the {20-2-1} major surfaces was 5 nm was thus obtained. Among these GaN crystal substrates were GaN crystal substrates whose major-surface plane orientation did not coincide perfectly with $\{20-2-1\}$, but the plane orientation of the major surface of such GaN crystal substrates in all cases was misoriented by 5° or less with respect to $\{20-2-1\}$.

Next, referring to FIG. **10**D, these GaN crystal substrates ²⁰ were disposed adjoining each other sideways, in a manner such that the (20-2-1) major surfaces **10**pm, **10**qm of the plurality of GaN crystal substrates (III-nitride crystal substrates **10**p, **10**q) would be parallel to each other, and such that the [0001] directions in the GaN crystal substrates would be ²⁵ oriented in the same way. In this instance, referring also to FIG. **10**C, the roughness average Ra of the adjoining faces **10**pt, **10**qt of the plurality of GaN crystal substrates was 5 nm.

With reference now to FIG. 10E: Next, the (20-2-1) major surfaces 10pm, 10qm of the situated plurality of GaN crystal 30 substrates (III-nitride crystal substrates 10p, 10q) were processed in the same way as that of Embodiment 1, and thereafter GaN crystal (III-nitride crystal 20) was grown onto the major surfaces 10pm, 10qm under the same conditions as those of Embodiment 1.

The obtained GaN crystal, free of abnormal growth even in the adjoining-substrate supra-regions 20t, had a (20-2-1) major surface 20m. In an x-ray diffraction rocking-curve analysis of the (20-2-1) plane of the GaN crystal (III-nitride crystal 20), the direct-over-substrate regions 20s demonstrated diffraction peaks undivided in the tip, with the full-width at half-maximum being 90 arcsec. In the adjoining-substrate supra-regions 20t, meanwhile, diffraction peaks with divisions in the tip were demonstrated, with the full-width at half-maximum being 360 arcsec. Also, with the x-ray 45 diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto the (20-2-1) plane, FWHM peak regions were present at intervals of almost 3 mm width, as indicated in FIG. 18.

Furthermore, threading dislocation density in the $(20\text{-}2\text{-}1)^{-50}$ major surface 20m of the GaN crystal was 1×10^7 cm⁻² in the direct-over-substrate regions 20s, and 4×10^7 cm⁻² in the adjoining-substrate supra-regions 20t. In addition, the carrier concentration in the GaN crystal was 1×10^{18} cm⁻³, while the principal impurities were oxygen atoms and silicon atoms. 55 The results are tabulated in Table I.

It should be understood that in Embodiment 11, the majorsurface plane orientations of the plurality of GaN crystal substrates that were the surfaces onto which the GaN crystal was grown were all (20-2-1), in that although a few or more 60 might have been (-202-1), the crystallographic equivalent of (20-2-1), they would lead to the same results.

Embodiment 12

To begin with, referring to FIG. 11A, the (0001) face and (000-1) face—the two major surfaces—of GaN bulk crystal

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(III-nitride bulk crystal 1) were ground and polished to bring the roughness average Ra of either major surface to 5 nm.

Next, again referring to FIG. 11A: The GaN bulk crystal (III-nitride bulk crystal 1) whose roughness average Ra on either of its major surfaces had been made 5 nm was sawed along a plurality of planes perpendicular to a <22-41> direction to slice off a plurality of GaN crystal substrates (IIInitride crystal substrates 10p, 10q) whose width S was 3 mm, length L was 20 to 50 mm, and thickness T was 1 mm, having {22-41} major surfaces. Subsequently, the not-ground and not-polished four sides of each sliced-off GaN crystal substrate were ground and polished, to bring the roughness average Ra of the four surfaces to 5 nm. A plurality of GaN crystal substrates whose roughness average Ra on the {22-41} major surfaces was 5 nm was thus obtained. Among these GaN crystal substrates were GaN crystal substrates whose majorsurface plane orientation did not coincide perfectly with {22-41}, but the plane orientation of the major surface of such GaN crystal substrates in all cases was misoriented by 5° or less with respect to {22-41}.

Next, referring to FIG. 11B, these GaN crystal substrates were disposed adjoining each other sideways, in a manner such that the (22-41) major surfaces 10pm, 10qm of the plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) would be parallel to each other, and such that the [0001] directions in the GaN crystal substrates would be oriented in the same way. In this instance, referring also to FIG. 11C, the roughness average Ra of the adjoining faces 10pt, 10qt of the plurality of GaN crystal substrates was 5 nm.

Next, with reference again to FIG. 11C: The (22-41) major surfaces 10pm, 10qm of the situated plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) were processed in the same way as that of Embodiment 1, and thereafter GaN crystal (III-nitride crystal 20) was grown onto the major surfaces 10pm, 10qm under the same conditions as those of Embodiment 1.

The obtained GaN crystal (III-nitride crystal **20**) had a (22-41) major surface **20***m* in which pits **20***v* defined by a plurality of facets **20***f* were formed in the adjoining-substrate supra-regions **20***t*. Further, in an x-ray diffraction rocking-curve analysis of the (22-41) plane of the GaN crystal (III-nitride crystal **20**), the direct-over-substrate regions **20***s* demonstrated diffraction peaks undivided in the tip, with the full-width at half-maximum being 220 arcsec. In the adjoining-substrate supra-regions **20***t*, meanwhile, diffraction peaks with divisions in the tip were demonstrated, with the full-width at half-maximum being 580 arcsec. Also, with the x-ray diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto the (22-41) plane, FWHM peak regions were present at intervals of almost 3 mm width, as indicated in FIG. **18**.

Furthermore, threading dislocation density in the (22-41) major surface 20m of the GaN crystal was 3×10^7 cm⁻² in the direct-over-substrate regions 20s, and 7×10^7 cm⁻² in the adjoining-substrate supra-regions 20t. In addition, the carrier concentration in the GaN crystal was 2×10^{18} cm⁻³, while the principal impurities were oxygen atoms and silicon atoms. The results are tabulated in Table I.

It should be understood that in Embodiment 12, the majorsurface plane orientations of the plurality of GaN crystal substrates that were the surfaces onto which the GaN crystal was grown were all (22-41), in that although a few or more might have been (-4221), the crystallographic equivalent of (22-41), they would lead to the same results.

Embodiment 13

To begin with, referring to FIG. 11A, the (0001) face and (000-1) face—the two major surfaces—of GaN bulk crystal

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(III-nitride bulk crystal 1) were ground and polished to bring the roughness average Ra of either major surface to 5 nm.

Next, again referring to FIG. 11A: The GaN bulk crystal (III-nitride bulk crystal 1) whose roughness average Ra on either of its major surfaces had been made 5 nm was sawed 5 along a plurality of planes perpendicular to a <22-4-1> direction to slice off a plurality of GaN crystal substrates (IIInitride crystal substrates 10p, 10q) whose width S was 3 mm, length L was 20 to 50 mm, and thickness T was 1 mm, having {22-4-1} major surfaces. Subsequently, the not-ground and not-polished four sides of each sliced-off GaN crystal substrate were ground and polished, to bring the roughness average Ra of the four surfaces to 5 nm. A plurality of GaN crystal substrates whose roughness average Ra on the {22-4-1} major surfaces was 5 nm was thus obtained. Among these 15 GaN crystal substrates were GaN crystal substrates whose major-surface plane orientation did not coincide perfectly with {22-4-1}, but the plane orientation of the major surface of such GaN crystal substrates in all cases was misoriented by 5° or less with respect to {22-4-1}.

Next, referring to FIG. 11D, these GaN crystal substrates were disposed adjoining each other sideways, in a manner such that the (22-4-1) major surfaces 10pm, 10qm of the plurality of GaN crystal substrates (III-nitride crystal substrates $\mathbf{10}p$, $\mathbf{10}q$) would be parallel to each other, and such that 25 the [0001] directions in the GaN crystal substrates would be conformed. In this instance, referring also to FIG. 11C, the roughness average Ra of the adjoining faces 10pt, 10qt of the plurality of GaN crystal substrates was 5 nm.

With reference now to FIG. 11E: Next, the (22-4-1) major 30 surfaces 10pm, 10qm of the situated plurality of GaN crystal substrates (III-nitride crystal substrates 10p, 10q) were pro-

cessed in the same way as that of Embodiment 1, and thereafter GaN crystal (III-nitride crystal 20) was grown onto the major surfaces 10pm, 10qm under the same conditions as those of Embodiment 1.

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The obtained GaN crystal (III-nitride crystal 20) had a (22-4-1) major surface 20m in which pits 20v defined by a plurality of facets 20f were formed in the adjoining-substrate supra-regions 20t. Further, in an x-ray diffraction rockingcurve analysis of the (22-4-1) plane of the GaN crystal (IIInitride crystal 20), the direct-over-substrate regions 20s demonstrated diffraction peaks undivided in the tip, with the full-width at half-maximum being 200 arcsec. In the adjoining-substrate supra-regions 20t, meanwhile, diffraction peaks with divisions in the tip were demonstrated, with the fullwidth at half-maximum being 480 arcsec. Also, with the x-ray diffraction FWHMs being measured along an axis defined by a < 0001 > direction of the substrate projected onto the (22-41) plane, FWHM peak regions were present at intervals of almost 3 mm width, as indicated in FIG. 18.

Furthermore, threading dislocation density in the (22-4-1) major surface 20m of the GaN crystal was 3×10^7 cm⁻² in the direct-over-substrate regions 20s, and 7×10⁷ cm⁻² in the adjoining-substrate supra-regions 20t. In addition, the carrier concentration in the GaN crystal was 2×10^{18} cm⁻³, while the principal impurities were oxygen atoms and silicon atoms. The results are tabulated in Table I.

It should be understood that in Embodiment 13, the majorsurface plane orientations of the plurality of GaN crystal substrates that were the surfaces onto which the GaN crystal was grown were all (22-4-1), in that although a few or more might have been (-422-1), the crystallographic equivalent of (22-4-1), they would lead to the same results.

			Emb. 1	Emb. 2	Emb. 3	Emb. 4	Emb. 5	Emb. 6	Emb. 7
crystal	Substrate type Major-surface		GaN (1 <u>1</u> 00)	GaN (1 <u>1</u> 00)	GaN (11 2 0)	GaN (1 <u>1</u> 02)	GaN (1 <u>1</u> 0 <u>2</u>)	GaN (11 <u>2</u> 2)	GaN (11 <u>22</u>)
substrate	plane orient.		-	-	-	-	-	-	-
	Major-surface roughness Ra (· · · · · ·	5	5	5	5	5	5	5
	Adjoining-surf		5	50	5	5	5	5	5
	roughness Ra (3	50	3	3	3	,	3
III-Nitride	Crystal type	mii)	GaN	GaN	GaN	GaN	GaN	GaN	GaN
crystal	Crystal growth	method	HVPE	HVPE	HVPE	HVPE	HVPE	HVPE	HVPE
0 1,5001	Crystal growth		1050	1050	1050	1050	1050	1050	1050
	temperature (°								
	Major-surface	<i>'</i>	$(1\overline{1}00)$	$(1\overline{1}00)$	$(11\overline{2}0)$	$(1\overline{1}02)$	$(1\overline{1}0\overline{2})$	$(11\overline{2}2)$	$(11\overline{22})$
	plane orient.								
	Pits present in	major surface?	No	Yes	Yes	No	No	No	No
	X-ray diff.	Direct-over-	100	100	250	120	100	90	80
	peak FWHM	subst. region							
	(arcsec)	Adjoinsub.	300	800	620	480	420	380	360
		supra-region							
	Major-surface	Direct-over-	1×10^{7}	1×10^{7}	1×10^{7}	1×10^{7}	1×10^{7}	1×10^{7}	1×10^{7}
	thread. disloc.	subst. region							
	dens. (cm ⁻²)	Adjoinsub. supra-region	3×10^{7}	8×10^{7}	8×10^7	6×10^{7}	6×10^{7}	4×10^{7}	4×10^{7}
	Carrier concent		5×10^{18}	5×10^{18}	5×10^{18}	5×10^{18}	5×10^{18}	5×10^{18}	5×10^{18}
	Oxygen/cm ³		4×10^{18}	4×10^{18}	2×10^{17}	3.2×10^{18}	4.9×10^{18}	2×10^{18}	3×10^{18}
	Silicon/cm ³		1×10^{18}	1×10^{18}	4.8×10^{18}	1.8×10^{18}	1×10^{17}	3×10^{18}	2×10^{18}
	Principal impu	rities	O, Si	O, Si	O, Si	O, Si	O, Si	O, Si	O, Si
				Emb. 8	Emb. 9	Emb. 10	Emb. 11	Emb. 12	Emb. 13
	III-Nitride Su	bstrate type		GaN	GaN	GaN	GaN	GaN	GaN
	crystal Ma substrate pla	ajor-surface		$(12\overline{3}0)$	(2350)	$(20\overline{2}1)$	(2021)	(2241)	(2241)
	M	ajor-surface		5	5	5	5	5	5
	Ac	ughness Ra (nm) djoining-surface ughness Ra (nm)		5	5	5	5	5	5

TABLE I-continued

III-Nitride	Crystal type		GaN	GaN	GaN	GaN	GaN	GaN
crystal	Crystal growth	method	HVPE	Flux	HVPE	HVPE	HVPE	HVPE
	Crystal growth		1050	870	1050	1050	1050	1050
	temperature (° 0	C.)						
	Major-surface		$(12\overline{3}0)$	$(23\overline{5}0)$	$(20\overline{2}1)$	$(20\overline{21})$	$(22\overline{4}1)$	$(22\overline{41})$
	plane orient.							
	Pits present in n	najor surface?	Yes	No	No	No	Yes	Yes
	X-ray diff.	Direct-over-	280	230	120	90	220	200
	peak FWHM	subst. region						
	(arcsec)	Adjoinsub.	660	490	380	360	580	480
		supra-region						
	Major-surface	Direct-over-	1×10^{7}	1×10^{7}	1×10^{7}	1×10^{7}	3×10^{7}	3×10^{7}
	thread. disloc.	subst. region	_	_	_	_	_	_
	dens. (cm ⁻²)	Adjoinsub.	7×10^{7}	4×10^{7}	4×10^{7}	4×10^{7}	7×10^{7}	7×10^{7}
		supra-region						
	Carrier concent	ration (cm ⁻³)	4×10^{18}	3×10^{18}	1×10^{18}	1×10^{18}	2×10^{18}	2×10^{18}
	Oxygen/cm ³		2×10^{18}	2.5×10^{18}	7×10^{17}	8×10^{17}	1×10^{18}	1.5×10^{18}
	Silicon/cm ³		2×10^{18}	5×10^{17}	3×10^{17}	2×10^{17}	1×10^{18}	5×10^{17}
	Principal impur	ities	O, Si	O, Si	O, Si	O, Si	O, Si	O, Si

As is evident from Table I, III-nitride crystal having a {h₀k₀i₀l₀} major surface was obtained by a III-nitride crystal manufacturing method including: a step of slicing III-nitride bulk crystal into a plurality of III-nitride crystal substrates having major surfaces of plane orientation {h₀k₀i₀l₀} other than {0001}, designated by choice; a step of disposing the plurality of III-nitride crystal substrates adjoining each other sideways so that the major surfaces of the plurality of IIInitride crystal substrates parallel each other and so that the way; and a step of growing III-nitride crystal onto the major surfaces of the plurality of III-nitride crystal substrates.

Herein, as indicated by Embodiments 1 through 13, the fact that $\{h_0k_0i_0l_0\}$ was—that the off-axis angle was—5° or less with respect to any crystallographically equivalent plane ori- 35 entation selected from the group consisting of $\{1-10x\}$ (wherein x is a whole number), {11-2y} (wherein y is a whole number), and {hk-(h+k)l} (wherein h, k and l are whole numbers) meant that III-nitride crystal of superior crystallinity, having a $\{h_0k_0i_0l_0\}$ major surface could be obtained. In 40 particular, as indicated by Embodiment 1, the fact that $\{h_0k_0i_0l_0\}$ was $\{1-100\}$ meant that III-nitride crystal of exceptionally superior crystallinity could be obtained.

Furthermore, as indicated by Embodiments 1 and 2, from the perspective of growing III-nitride crystal stably, it is pref-45 erable that the roughness average Ra of the surfaces along which the plurality of III-nitride crystal substrates adjoin be 50 nm or less, more preferably 5 nm or less.

Embodiment 14

Crystal of (1-102) orientation, manufactured in Embodiment 4, was processed into a 0.4-mm thick, 2-inch diameter wafer, onto which, referring to FIG. 12, an LED structure 55 was formed by MOCVD to fabricate a light-emitting diode 55 (LED) as a light-emitting semiconductor device. In this instance, for growing the plurality of III-nitride crystal layers that constituted the LED structure, trimethyl gallium (TMG), trimethyl indium (TMI), and/or trimethyl aluminum (TMA) were utilized as the Group-III source material; ammonia was 60 utilized as the nitrogen source material; monosilane was utilized as the n-type dopant source material; and bis(cyclopentadienyl)magnesium (CP2Mg) was utilized as the p-type dopant source material.

Specifically, as the plurality of III-nitride crystal layers 65 constituting the LED structure 55, an n-type GaN layer 51 of 2 μm thickness, a multiquantum well (MQW) light-emitting

layer 52 of 88 nm thickness (seven In_{0.01}GaN barrier layers 52b of 10 nm thickness and six $In_{0.14}GaN$ well layers 52w of 3 nm thickness, arranged in alternating layers), a p-type Al_{0.18}GaN electron-blocking layer 53 of 20 nm thickness, and a p-type GaN contact layer 54 of 50 nm thickness were grown, in that order, by MOCVD onto the major surface of a GaN crystal substrate 20, with the (1-102) plane being the major surface.

As a p-side electrode 56, a transparent ohmic contact, [0001] directions in the substrates be oriented in the same 30 constituted from Ni (5 nm)/Au (10 nm) and measuring 400 μm lengthwise×400 μm widthwise×15 nm in thickness, was formed by vacuum evaporation deposition onto the p-type GaN contact layer 54. Then, as an n-side electrode 57, an ohmic contact, constituted from Ti (20 nm)/Al (300 nm) and measuring 400 um lengthwise×400 um widthwise×320 nm in thickness, was formed by vacuum evaporation deposition onto the wafer major surface on the reverse side from the MOCVD-growth side. The wafer was then segmented into chips measuring 500 µm lengthwise×500 µm widthwise to complete the LEDs.

> The thus obtained LEDs, with emission wavelength of 420 nm and, with an emission intensity of 4 mW to 5 mW when a 20 mA current was applied, demonstrating outstanding light output intensity, were ideally suited to LED applications.

Embodiment 15

Crystal of (20-21) orientation, manufactured in Embodiment 10, was processed into a 0.4-mm thick, 2-inch diameter 50 wafer, onto which, referring to FIG. 13, an LD structure 70 was formed by MOCVD to fabricate a laser diode (LD) as a light-emitting semiconductor device. In this instance, for growing the plurality of III-nitride crystal layers that constituted the LD structure, trimethyl gallium (TMG), trimethyl indium (TMI), and/or trimethyl aluminum (TMA) were utilized as the Group-III source material; ammonia was utilized as the nitrogen source material; monosilane was utilized as the n-type dopant source material; and bis(cyclopentadienyl) magnesium (CP2Mg) was utilized as the p-type dopant source material.

Specifically, as the plurality of III-nitride crystal layers constituting the LD structure 70, an n-type Al_{0.04}GaN layer 61 of 2 µm thickness, an n-type GaN layer 62 of 50 nm thickness, an undoped In_{0.03}GaN layer **63** of 65 nm thickness, a multiquantum well (MQW) light-emitting layer 64 (three In_{0.02}GaN barrier layers **64**b of 15 nm thickness and three In_{0.30}GaN well layers 64w of 3 nm thickness, arranged in

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alternating layers), an undoped $In_{0.03}GaN$ layer **65** of 50 nm thickness, a undoped GaN layer **66** of 50 nm thickness, a p-type $Al_{0.18}GaN$ layer **67** of 20 nm thickness, a p-type $Al_{0.06}GaN$ layer **68** of 400 nm thickness, and a p-type GaN contact layer **69** of 50 nm thickness were grown, in that order, by MOCVD onto the major surface of a GaN crystal substrate **20**, with the (20-21) plane being the major surface.

As a p-side electrode 71, an ohmic contact, constituted from Ni (20 nm)/Au (200 nm) and measuring 10 μm lengthwise×400 μm widthwise×220 nm in thickness, was formed by vacuum evaporation deposition onto the p-type GaN contact layer 69. Then, as an n-side electrode 72, an ohmic contact, constituted from Ti (20 nm)/Al (300 nm) and measuring 400 μm lengthwise×400 μm widthwise×320 nm in thickness, was formed by vacuum evaporation deposition onto the wafer major surface on the reverse side from the MOCVD-growth side. The wafer was then cleaved along the (1-120) plane into chips measuring 300 μm lengthwise×400 μm widthwise to complete the LDs.

The thus obtained LDs, demonstrating, with a lasing 20 threshold current of 420 mA and a lasing wavelength of 520 nm, outstanding device properties, were optimal as green LDs.

Embodiment 16

Crystal of (11-20) orientation, manufactured in Embodiment 3, was processed into a 0.4-mm thick, 2-inch diameter wafer, onto which, referring to FIG. 14, a Schottky barrier diode structure was formed by MOCVD to fabricate a Schottky barrier diode (SBD) as a semiconductor electronic device. In this instance, for growing the III-nitride crystal layer that constituted the SBD structure, trimethyl gallium (TMG), was utilized as the Group-III source material, ammonia as the nitrogen source material, while monosilane was utilized as 35 the n-type dopant source material.

Specifically, as the III-nitride crystal layer constituting the SBD structure, an n-type GaN layer **81** of 10 µm thickness was grown by MOCVD onto the major surface of a GaN crystal substrate **20**, with the (11-20) plane being the major 40 surface.

As a p-side electrode **82**, a Schottky contact, constituted from Ni (20 nm)/Au (200 nm) and measuring $400 \, \mu \text{m}$ lengthwise× $400 \, \mu \text{m}$ widthwise× $220 \, \text{nm}$ in thickness, was formed by vacuum evaporation deposition onto the surface of the epitaxial layer that had been deposited by MOCVD. In turn, as an n-side electrode **83**, an ohmic contact, constituted from Ti $(20 \, \text{nm})/\text{Al}$ $(300 \, \text{nm})$ and measuring $400 \, \mu \text{m}$ lengthwise× $400 \, \mu \text{m}$ widthwise× $320 \, \text{nm}$ in thickness, was formed by vacuum evaporation deposition onto the wafer major surface on the reverse side from the MOCVD-growth side. The wafer was then segmented into chips measuring 5 mm lengthwise× $5 \, \text{mm}$ widthwise to complete fifty-two SBDs.

The blocking voltage of the obtained fifty-two SBDs was measured, whereupon the maximum was 1500 V and the 55 minimum was 680 V, with the average being 1020 V. In addition, with a 50-amp on-resistance being a minimum 0.6 $m\Omega\text{-cm}^2$ and a maximum 3 $m\Omega\text{-cm}^2$ with the average being 0.8 $m\Omega\text{-cm}^2$, the devices had outstanding characteristics and thus were ideally suited as Schottky barrier diodes. 60 III-Nitride Bulk Crystal Preparation 2

AlN bulk crystal, as III-nitride bulk crystal utilized in a III-nitride crystal manufacturing method involving the present invention, was manufactured by a method as below.

To begin with, AlN bulk crystal was deposited by sublimation growth onto the (0001)-plane major surface of, as an undersubstrate, an SiC substrate of 51 mm diameter and 0.5

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mm thickness. During the growth of the AlN bulk crystal, until it had grown to 0.5 mm thickness, 0.1 mass % $\rm CO_2$ gas (Group-IV element-containing gas) was supplied, with the growth temperature made 1700° C., to dope the crystal with carbon atoms as Group IV dopants. Thereafter, while a growth temperature of 1800° C. was sustained, the supply of the Group-IV element-containing gas was stopped, and an AlN bulk crystal of 5.5 mm thickness (including the just-noted 0.5-mm thick portion doped with carbon atoms) was grown. On the (0001) face of the grown AlN bulk crystal, a plurality of hexagonal pyramidal pits defined by plural facets was formed.

Next, employing a mechanical polish, the SiC substrate was removed from the above-described AlN bulk crystal, yielding a 50-mm diameter, 3-mm thick AlN bulk crystal as III-nitride bulk crystal. At that time, the portion doped with Group IV dopants (carbon atoms) by supplying a Group-IV element containing gas, and grown 5 mm thick, was eliminated.

Embodiment 17

To begin with, referring to FIG. 3A, the (0001) face and (000-1) face—the two major surfaces—of AlN bulk crystal (III-nitride bulk crystal 1) were ground and polished to bring the roughness average Ra of either major surface to 5 nm.

Next, again referring to FIG. 3A: The AlN bulk crystal whose roughness average Ra on either of its major surfaces had been made 5 nm was sawed along a plurality of planes perpendicular to a <1-100> direction to slice off a plurality of AlN crystal substrates (III-nitride crystal substrates 10p, 10q) whose width S was 3 mm, length L was 20 to 50 mm, and thickness T was 1 mm, having {1-100} major surfaces. Subsequently, the not-ground and not-polished four sides of each sliced-off AlN crystal substrate were ground and polished, to bring the roughness average Ra of the four surfaces to 5 nm. A plurality of AlN crystal substrates whose roughness average Ra on the {1-100} major surfaces was 5 nm was thus obtained. Among these AlN crystal substrates were AlN crystal substrates whose major-surface plane orientation did not coincide perfectly with {1-100}, but the plane orientation of the major surface of such AlN crystal substrates in all cases was misoriented by 5° or less with respect to $\{1-100\}$.

Next, referring to FIG. 3B, these AlN crystal substrates were disposed adjoining each other sideways, in a manner such that the (1-100) major surfaces 10pm, 10qm of the plurality of AlN crystal substrates (III-nitride crystal substrates 10p, 10q) would be parallel to each other, and such that the [0001] directions in the AlN crystal substrates would be oriented in the same way. In this instance, referring also to FIG. 3C, the roughness average Ra of the adjoining faces 10pt, 10qt of the plurality of AlN crystal substrates was 5 nm.

Next, with reference again to FIG. 3C, under a gaseous nitrogen ambient at 2200° C. AlN crystal (III-nitride crystal 20) was deposited by sublimation growth onto the (1-100) major surfaces 10pm, 10qm of the situated plurality of AlN crystal substrates (III-nitride crystal substrates 10p, 10q) for 50 hours at a deposition rate of $100 \,\mu\text{m/hr}$.

The obtained AIN crystal (III-nitride crystal **20**), free of abnormal growth even in the adjoining-substrate supra-regions **20***t*, had a (1-100) major surface **20***m*. The crystallinity of the AIN crystal (III-nitride crystal **20**) was characterized by an x-ray diffraction rocking-curve analysis of the (1-100) plane. With this AIN crystal, the direct-over-substrate regions **20***s* demonstrated diffraction peaks undivided in the tip, with the full-width at half-maximum being 30 arcsec. Likewise, in the adjoining-substrate supra-regions **20***t* as well, diffraction

peaks without divisions in the tip were demonstrated, with the full-width at half-maximum being 50 arcsec. Also, with the x-ray diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto the (1-100) plane, FWHM peak regions were present at intervals of almost 3 mm width, as indicated in FIG. 18.

Furthermore, in the following manner the threading dislocation density in the (1-100) major surface 20m of the AlN crystal was determined. Namely, an AlN wafer (III-nitride crystal wafer 21), as represented in FIG. 3C, characterized by an area where the (1-100) plane was broadest was sliced off. This AlN wafer (III-nitride crystal wafer 21) was heated to 250° C., and the (1-100) major surface was etched by immersion of the wafer for one hour in a molten KOH—NaOH liquefied mixture (in mass ratio, KOH:NaOH=50:50). By observation of the (1-100) major surface of the etched AlN wafer (III-nitride crystal wafer 21) under an optical microscope, the number of etch pits within a $100 \ \mu m \times 100 \ \mu m$ square surface was counted to calculate the etch-pit density (EPD) as the major-surface threading dislocation density.

The threading dislocation density of the (1-100) major surface 20m of the above-described AlN crystal was 1×10^5 cm⁻² in the direct-over-substrate region 20s, and 2×10^5 cm⁻² in the adjoining-substrate supra-regions 20t. Meanwhile, the principal impurities in the AlN crystal according to SIMS 25 (secondary ion mass spectrometry) were oxygen atoms and carbon atoms. The results are tabulated in Table II.

It should be understood that in Embodiment 17, the majorsurface plane orientations of the plurality of AlN crystal substrates that were the surfaces onto which the AlN crystal was 30 grown were all (1-100), in that although a few or more might have been (-1100), the crystallographic equivalent of (1-100), they would lead to the same results.

Embodiment 18

To begin with, referring to FIG. 3A, the (0001) face and (000-1) face—the two major surfaces—of AlN bulk crystal were ground and polished to bring the roughness average Ra of either major surface to 50 nm.

Next, again referring to FIG. 3A: The AlN bulk crystal whose roughness average Ra on either of its major surfaces had been made 50 nm was sawed along a plurality of planes perpendicular to a <1-100> direction to slice off a plurality of AlN crystal substrates (III-nitride crystal substrates 10p, 10q) 45 whose width S was 3 mm, length L was 20 to 50 mm, and thickness T was 1 mm, having $\{1\text{-}100\}$ major surfaces. Subsequently, the not-ground and not-polished four sides of each sliced-off AlN crystal substrate were ground and polished, to

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bring the roughness average Ra of the four surfaces to 5 nm. A plurality of AlN crystal substrates whose roughness average Ra on the {1-100} major surfaces was 5 nm was thus obtained. Among these AlN crystal substrates were AlN crystal substrates whose major-surface plane orientation did not coincide perfectly with {1-100}, but the plane orientation of the major surface of such AlN crystal substrates in all cases was misoriented by 5° or less with respect to {1-100}.

Next, referring to FIG. 3B, these AlN crystal substrates were disposed adjoining each other sideways, in a manner such that the (1-100) major surfaces 10pm, 10qm of the plurality of AlN crystal substrates (III-nitride crystal substrates 10p, 10q) would be parallel to each other, and such that the [0001] directions in the AlN crystal substrates would be oriented in the same way. In this instance, referring also to FIG. 3C, the roughness average Ra of the adjoining faces 10pt, 10qt of the plurality of AlN crystal substrates was 50 nm.

Next, with reference again to FIG. 3C, under a gaseous nitrogen ambient at 2200° C. AlN crystal (III-nitride crystal 20 20) was deposited by sublimation growth onto the (1-100) major surfaces 10pm, 10qm of the situated plurality of AlN crystal substrates (III-nitride crystal substrates 10p, 10q) for 50 hours at a deposition rate of 100 µm/hr.

The obtained AlN crystal (III-nitride crystal 20), free of abnormal growth even in the adjoining-substrate supra-regions 20t, had a (1-100) major surface 20m. In an x-ray diffraction rocking-curve analysis of the (1-100) plane of the AlN crystal, the direct-over-substrate regions 20s demonstrated diffraction peaks undivided in the tip, with the fullwidth at half-maximum being 100 arcsec. Likewise, in the adjoining-substrate supra-regions 20t as well, diffraction peaks without divisions in the tip were demonstrated, with the full-width at half-maximum being 150 arcsec. Also, with the x-ray diffraction FWHMs being measured along an axis defined by a <0001> direction of the substrate projected onto the (1-100) plane, FWHM peak regions were present at intervals of almost 3 mm width, as indicated in FIG. 18. Furthermore, the threading dislocation density of the (1-100) major surface 20m of the AlN crystal was 3×10^5 cm⁻² in the direct-40 over-substrate region 20s, and 4×10^5 cm⁻² in the adjoiningsubstrate supra-regions 20t. Meanwhile, the principal impurities in the AlN crystal were oxygen atoms and carbon atoms. The results are tabulated in Table II.

It should be understood that in Embodiment 18, the majorsurface plane orientations of the plurality of AlN crystal substrates that were the surfaces onto which the AlN crystal was grown were all (1-100), in that although a few or more might have been (-1100), the crystallographic equivalent of (1-100), they would lead to the same results.

TABLE II

			Emb. 17	Emb. 18
III-Nitride	Substrate type		AlN	AlN
crystal	Major-surface plane ori	entation	(1 1 00)	(1 1 00)
substrate	Major-surface roughnes	ss Ra (nm)	5	5
	Adjoining-surface roug	hness Ra (nm)	5	50
III-Nitride	Crystal type		AlN	AllIN
crystal	Crystal growth method	Sublimation	Sublimation	
•	Crystal growth tempera	ture (° C.)	2200	2200
	Major-surface plane ori	entation	$(1\overline{1}00)$	$(1\overline{1}00)$
	Pits present in major su	rface?	No	No
	X-ray diffraction peak FWHM (arcsec)	Direct-over-substrate region	30	100
	` ,	Adjoining-substrate supra-region	50	150
	Major-surface threading dislocation	Direct-over-substrate region	1×10^{5}	3×10^{5}

31 TABLE II-continued

	Е	imb. 17 I	Emb. 18
density (cm ⁻²) Adjoir supra-		2×10^5	4 × 10 ⁵
Carrier concentration (cm ⁻³)	_	_	_
Principal impurities		O, C	O, C

As is evident from Table II, III-nitride crystal having a 10 tor sensors (such as temperature sensors, pressure sensors, $\{h_0k_0i_0l_0\}$ major surface was obtained by a III-nitride crystal manufacturing method including: a step of slicing III-nitride bulk crystal into a plurality of III-nitride crystal substrates having major surfaces of plane orientation {h₀k₀i₀l₀} other than {0001}, designated by choice; a step of disposing the 15 substrates adjoining each other sideways so that the major surfaces of the plurality of III-nitride crystal substrates parallel each other and so that the [0001] directions in the substrates be oriented in the same way; and a step of growing III-nitride crystal onto the major surfaces of the plurality of 20 III-nitride crystal substrates.

Herein, as is evident from a comparison between Embodiments 1 through 7 in Table I and Embodiments 17 and 18 in Table II, it was found that in the III-nitride crystal manufacturing methods, having the temperature at which the III- 25 nitride crystal is grown be 2000° C. or greater remarkably reduces the threading dislocation density in the major surface of the III-nitride crystal.

The presently disclosed embodying modes and embodiment examples should in all respects be considered to be 30 illustrative and not limiting. The scope of the present invention is set forth not by the foregoing description but by the scope of the patent claims, and is intended to include meanings equivalent to the scope of the patent claims and all modifications within the scope.

Group III-nitride crystal manufactured by a manufacturing method involving the present invention is utilized in applications including optical elements (such as light-emitting diodes and laser diodes), in semiconductor electronic devices (such as rectifiers, bipolar transistors, field-effect transistors, 40 or high electron mobility transistors (HEMTs)), semiconduc-

radiation sensors, or visible-blind ultraviolet detectors), surface acoustic wave devices (SAW devices), acceleration sensors, microelectromechanical system (MEMS) parts, piezoelectric oscillators, resonators, and piezoelectric actuators.

What is claimed is:

- 1. A Group-III nitride crystal substrate having, on opposite sides of the substrate, major surfaces of plane orientation other than {0001}, the III-nitride crystal substrate character
 - by a property that with x-ray diffraction full-width-at-halfmaximums (FWHMs) being measured along an axis defined by a <0001> direction of the substrate projected onto either of the major surfaces, FWHM peak regions are present at inter-peak-region intervals of 3 to 5 mm width: and
 - in being of plane orientation selected from the group consisting of $\{1-100\}$, $\{11-20\}$, $\{1-10\pm2\}$, $\{11-2\pm2\}$, $\{20-10\pm2\}$ 2 ± 1 and $\{22-4\pm 1\}$.
- 2. The III-nitride crystal substrate of claim 1, further characterized by a property that with threading dislocation density being measured along a <0001> direction of the III-nitride crystal substrate, threading-dislocation-density peak regions are present at inter-peak-region intervals of 3 to 5 mm width.
- 3. The III-nitride crystal substrate of claim 1, further characterized by containing, as principal impurities, either oxygen atoms or silicon atoms.
- 4. The III-nitride crystal substrate of claim 1, further characterized by a property of having x-ray diffraction peaks whose tips are divided.